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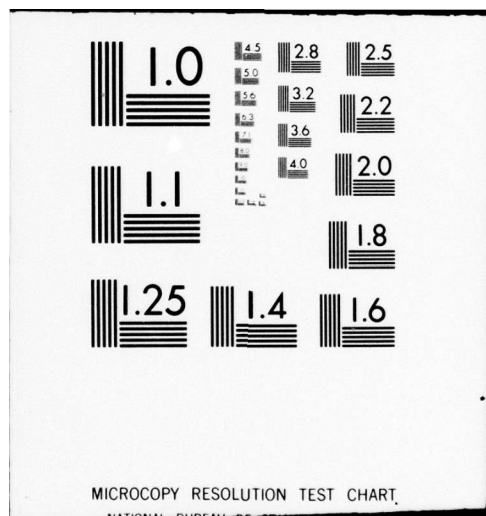
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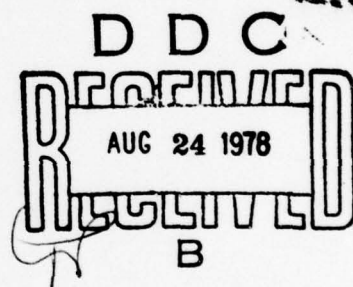
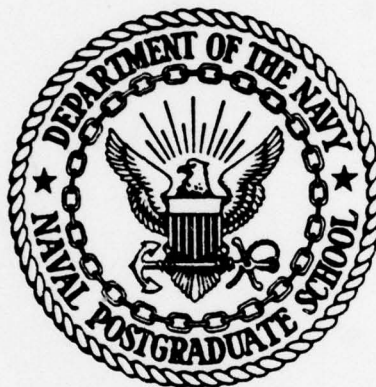


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THESIS

DESIGN OF SOFTWARE PACKAGE FOR INCORPORATION
OF RANDOM LOAD TESTING AND DATA PROCESSING
ON MATERIALS TESTING SYSTEM MACHINE

by

Frederick Martin Blakely, Jr.

June 1978

Thesis Advisor:

G. H. Lindsey

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Design of Software Package for Incorporation
of Random Load Testing and Data Processing
on Materials Testing System Machine

by

Frederick Martin Blakely
Lieutenant Commander, United States Navy
B.S., Naval Postgraduate School, 1970

Submitted in partial fulfillment of the
requirements for the degree of

MASTER OF SCIENCE IN AERONAUTICAL ENGINEERING

from the

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ABSTRACT

This thesis describes the software design and implementation of a microprocessor-based, random load drive and data acquisition system on a Material Testing System (MTS) machine.

A microprocessor, combination analog input/output module, magnetic cassette tape recorder, and strain gage network form a strain data acquisition system for recording sequential strain peaks and troughs on specimens subjected to flight load histories. The data will be used to estimate the fraction of the fatigue life expended in a test specimen.

A high speed digital computer is linked by telephone line to a microcomputer development system to create the randomization of fatigue loads specified in Mil Spec 8866 Spectrum A for use by the MTS machine.

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I. INTRODUCTION

With the greater complexity and cost of new weapon systems, together with the general economic pressures to control expenditures, there is an urgent need to obtain maximum use from the operational lives of aircraft.

Currently, fatigue monitoring of naval aircraft is based on the total number of g readings recorded at four selected levels by an exceedence level counting accelerometer. Using microprocessors, it will soon be possible to record in sequence each maximum and minimum load level experienced by an aircraft. The data collected can be used to monitor the fatigue life of a structure via the determination of damage accumulated at a point found to be critical in a structural test of a prototype.

The objective of this research work was to design a software package to incorporate random load testing and data processing on a Materials Testing System (MTS) machine (Figure 1). The random load history would come from either monitored flight data or computer generated sequences.

The entire software package was written in two phases. The first phase consists of software for a single channel strain data acquisition system. This acquisition system is designed to process and record in sequence data originating from strain gages located at fatigue critical points. The data obtained will be sequential peaks and troughs that will be used to estimate the fraction of the fatigue life of the structure that has been expended.

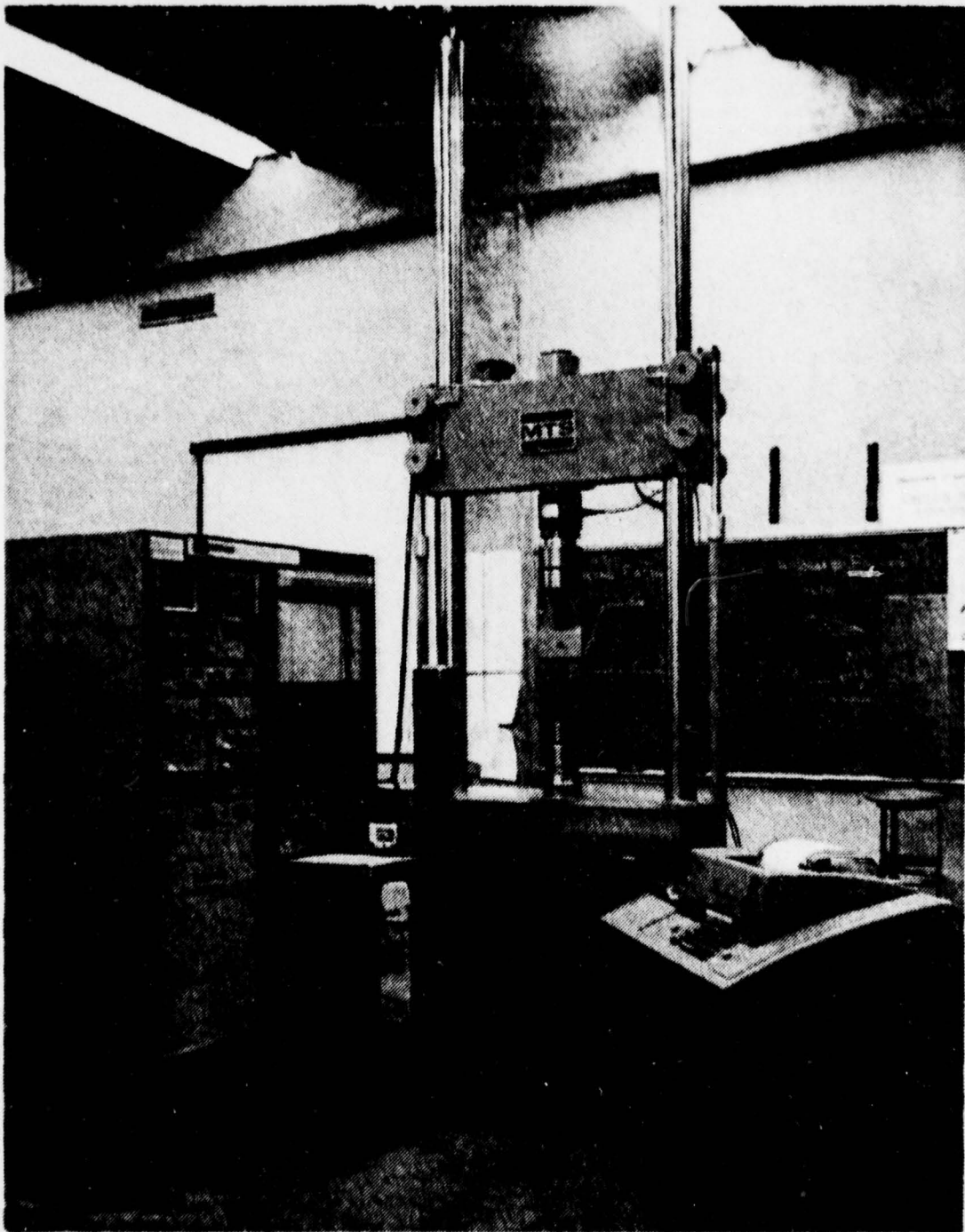


Figure 1. Materials Testing System (MTS)

The system developed is composed of two major subsystems: the Write subsystem and the Read subsystem. The Write subsystem is tasked with the collection of data. It makes use of a microprocessor, analog to digital converter, a magnetic tape recorder, and a strain gage network to monitor strain-generated signals, identify significant events and record the collected data. The Read subsystem is tasked with the retrieval and display of the data from the Write subsystem recorder.

Phase two of the software package consists of software necessary to incorporate randomization of MIL SPEC 8866 SPECTRUM A loads into a material testing system.

This system creates random loads on the IBM 360 computer, transmits load data via telephone line to the MDS 800 microcomputer development system, and finally punches the load data on punch paper tape, which supplies the random load sequence for the MTS.

The entire software package represents a smooth interface between the high level Fortran language used by the IBM system 360 and the low level assembly language of the system 80/10 microcomputer.

A glossary of terms commonly used in the instrumentation engineering, data processing and computing disciplines is presented in Appendix A.

II. STRAIN DATA RECORDING

A. HARDWARE COMPONENTS

1. Intel System 80/10 Microcomputer

The Intel 80/10 Microcomputer System is self-contained, utilizing the SBC-80/10 single board computer. The standard system 80/10 contains 1K (1K - 1024 bytes) of 8-bit read/write, Random Access Memory (RAM). Sockets for up to 4K of 8-bit words of non-volatile Read-Only-Memory (ROM) are provided in the system. The 8-bit Intel 8080A CPU is the central processor for the system 80/10. [Figure 2].

The 8080A contains six 8-bit general purpose registers and an accumulator. The six general purpose registers may be addressed individually or in pairs, providing both single and double precision operators.

The 8080A has a 16-bit program counter, which allows direct addressing of up to 64K bytes of memory. An external stack at memory location 7FFFH may be used as a last in/first out stack to store the contents of the program counter, flags, accumulator, and all of the six general purpose registers. A 16-bit stack pointer addresses the external stack. This provides subroutine nesting that is bounded only by memory size.

The system 80/10 contains 48 programmable parallel input/output (I/O) lines implemented by two Intel 8255 Programmable Peripheral Interface (PPI) devices. Software is used to configure the I/O lines in combinations of unidirectional

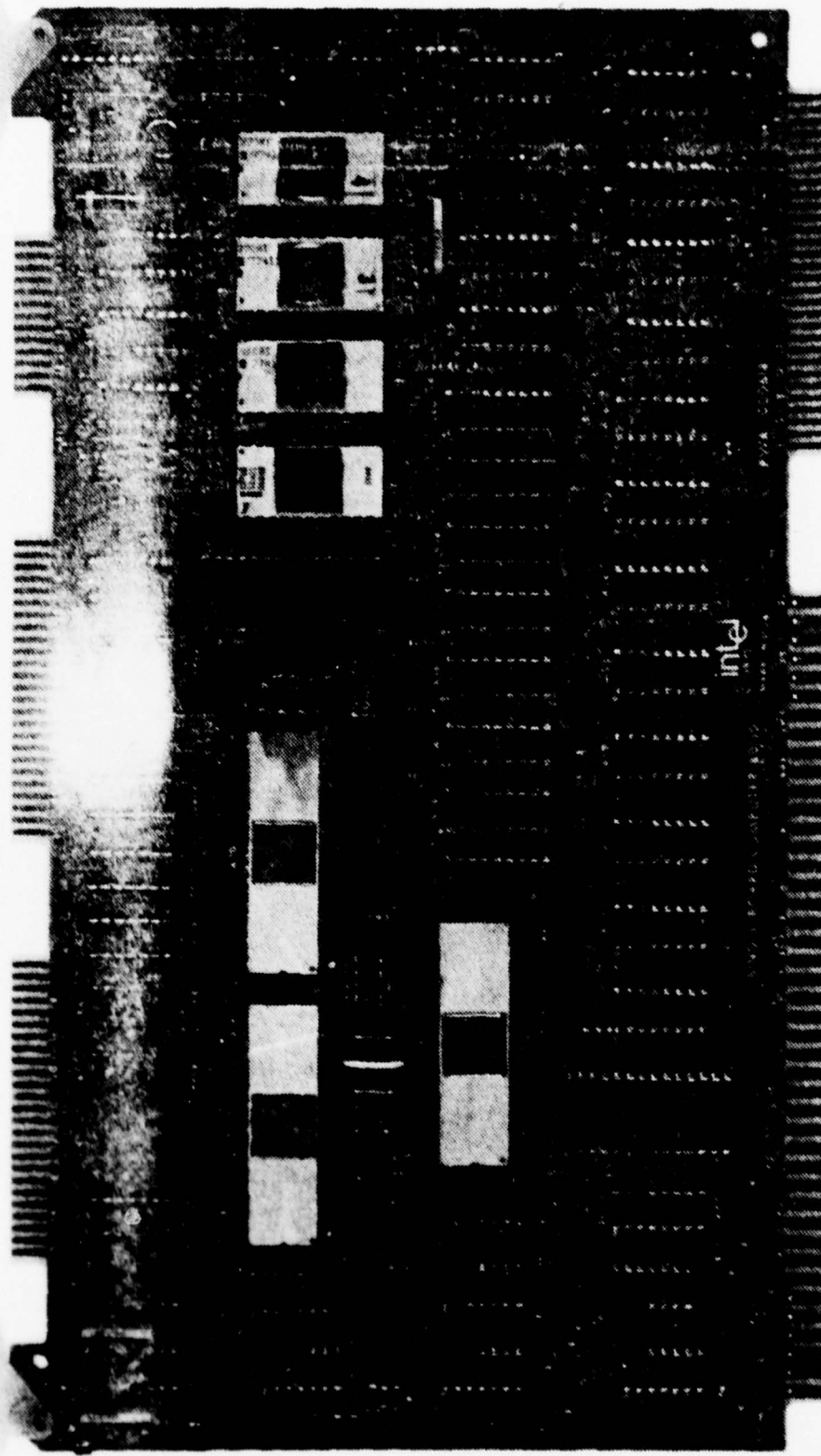


Figure 2. Single Board Computer 80/10.

parts and bidirectional parts. Section 3 will discuss in detail the setup of the Intel 8255 PPI devices used for this thesis.

2. System 80/10 Monitor Program

A standard feature of the system 80/10 is a software monitor, which is programmed in two ROM's. The monitor provides two basic capabilities: (1) it accesses console input/output routines as well as paper tape input/output control software; (2) the monitor, along with a teletype (TTY) or a cathode ray tube (CRT), provides the user with a console that furnishes immediate access to both memory and registers. It also has control commands to begin execution, display, or alter the contents of the memory or registers. The system monitor ROM's are positioned in the first two ROM sockets of the SBC-80/10 computer, occupying memory locations 0 to 2048. A more complete description of monitor commands is given in Ref. [1].

3. 8255 Programmable Peripheral Interface Device

The peripheral interface section of Group 2 contains 24 peripheral interface lines, buffers, and control logic. The characteristics and functions of the interface lines are determined by the operating mode selected under program control; three modes of operation may be selected:

MODE 0 - BASIC INPUT/OUTPUT

MODE 1 - STROBED INPUT/OUTPUT WITH INTERRUPT SUPPORT

MODE 2 - BIDIRECTIONAL BUS WITH INTERRUPT SUPPORT

Table I lists the basic features of the peripheral interface lines within each mode group. This thesis will utilize the mode 0 basic input/output mode only.

TABLE I
Features of Peripheral Interface Lines

Mode 0 – Basic Input/Output
Two 8-bit ports Two 4-bit ports with bit set/reset capability Outputs are latched Inputs are not latched
Mode 1 – Strobed Input/Output
One or two strobed ports Each Mode 1 port contains: 8-bit data port 3 control lines Interrupt support logic Any port may be input or output If one Mode 1 port is used, the remaining 13 lines may be configured in Mode 0. If two Mode 1 ports are used, the remaining 2 bits may be input or output with bit set/reset capability.
Mode 2 – Strobed Bidirectional Bus
One bidirectional bus which contains: 8-bit bidirectional bus supported by Port A 5 control lines Interrupt support logic Inputs and outputs are latched The remaining 11 lines may be configured in either Mode 0 or Mode 1.

The mode definition control word shown in Figure 3 is used to specify the configuration of the peripheral interface lines on the 8255 device. When the opcode field (bit 7) of the control word is equal to one, the control word is interpreted by the 8255 as a mode definition control word. The system software may specify the modes of port A and B independently as input or output when in mode 0. Port C may be treated as input only, output only, or divided into two portions of input and output, as required by the port A and port B mode definitions.

In group 2 the 8255 chip, with the control register address of EBH, was configured through the use of the mode control word interface as:

```
PORT A - MODE 0  OUTPUT
PORT B - MODE 0  INPUT
PORT C - MODE 0  INPUT (STATUS)
PORT C - MODE 0  OUTPUT (CONTROL)
```

The following mode control word was used:

```
1 0 0 0   0 0 1 1   (Binary)
      8       3       (Hex)
```

The assembly language program is:

```
CONTROL EQU OEBH; 8255 #2 ADDRESS
MVI     A,83H    ; move control word into ACCM
OUT     CONTROL  ; output to address OEBH
```

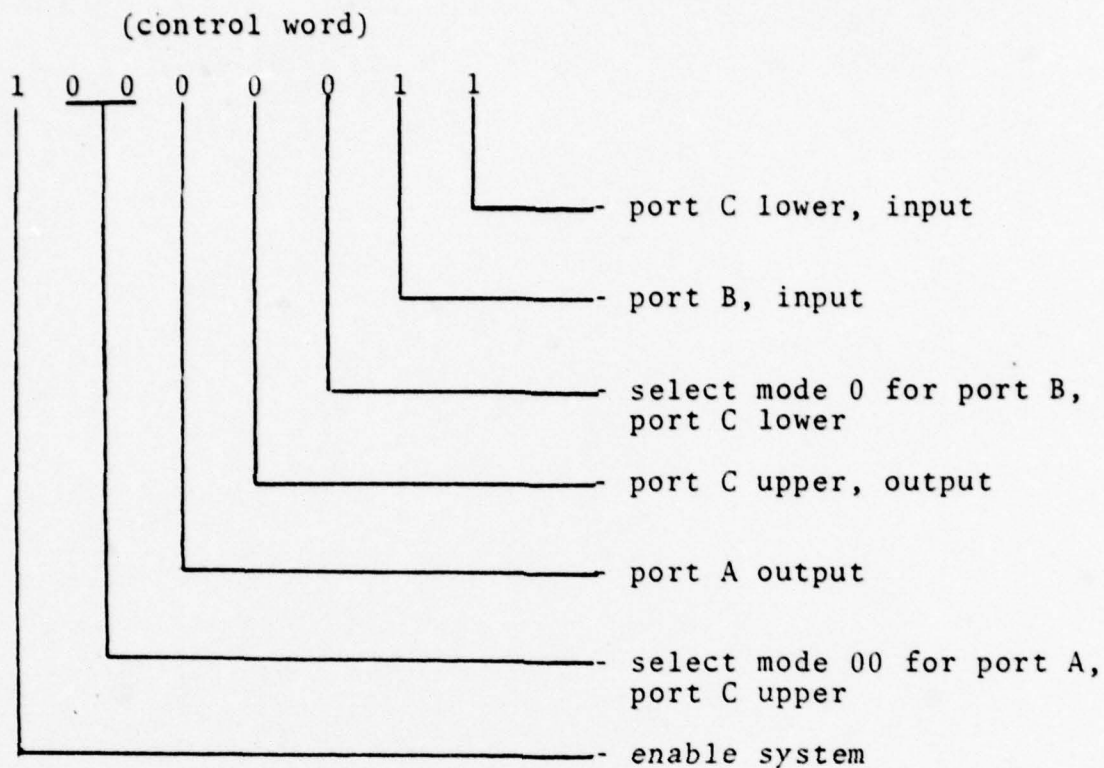


Figure 3. 8255 #2 Mode Control Word.

Now, let's take a closer look at port C since its functions are split between control and status monitoring. The address of port C is 0EAH, as shown in Table II. For example, 11110000, or 0F0H, will put the recorder in the Write mode and start writing characters. The assembly language program follows:

```

PORT C    EQU    0EAH;  PORT C    ADDRESS
MVI       A,0F0H    ;  PORT C    CONTROL WORD
OUT       PORT C    ;

```


TABLE II
Port Assignment 8255

SBC-80/10 ADDR	J2	Cable DB25	(Tape) Memodyne	Name/Function
Port A	43	2	M	2 ⁰
	45	3	N	2 ¹
	47	4	U	2 ²
	49	5	V	2 ³
	51	6	W	2 ⁴
	39	7	X	2 ⁵
EB	37	8	Y	2 ⁶
	35	9	19	2 ⁷
Port B	05	14	2	2 ⁰
	7	15	C	2 ¹
	9	16	F	2 ²
	3	17	H	2 ³
	11	18	J	2 ⁴
	13	19	K	2 ⁵
E9	15	20	10	2 ⁶
	17	21	11	2 ⁷
STATUS				
Port C	25	25	20	STATUS
	23	10	8	TAPE SYNC
EA	21	11	S	CIP
	19	12	17	BEOT
CONTROL				
Port C	27	13	A	LWD FWD
	29	22	L	RWD
EA	31	23	12	START/STOP
	33	24	22	WR/RD
		1	14	GND

4. SBC-732 Combination Analog Input/Output Board

The SBC-732 is an analog input/output subsystem which, under microprocessor control, performs the basic functions of data acquisition of analog inputs and controlled analog output signals [Fig. 4]. There are three programmable modes of operation for the acquisition of analog inputs: repetitive single input, sequential channel scan input, and random channel input.

The 732 multiplexer can accommodate 8 differential or 16 single-ended analog input channels. All input channels are pretested to $\pm 28V$ by clamping diodes and fusible current-limit resistors.

The selected differential or single-ended input is applied to the analog-to-digital connection (ADC) via a programmable gain amplifier, which under program control was selected to provide a gain of 1. The ADC is a 12-bit, 35.7 microsecond, successive approximation device with an internal sample-and-hold (S/H) amplifier. The ADC was jumper selected for $\pm 10V$ full scale inputs. The A/D conversion process was initiated by program command.

Each of the two 12-bit non-isolated digital-to-analog converters (DAC's) were configured for ± 10 volts full-scale voltage output. Table III contains SBC-732 board specifications.

5. Memodyne Model 173 Cassette Recorder

The recorder is a memodyne model 173 magnetic tape recorder. The model 173 is a parallel input/output, read/write unit designed to be compatible with ASCII requirements.

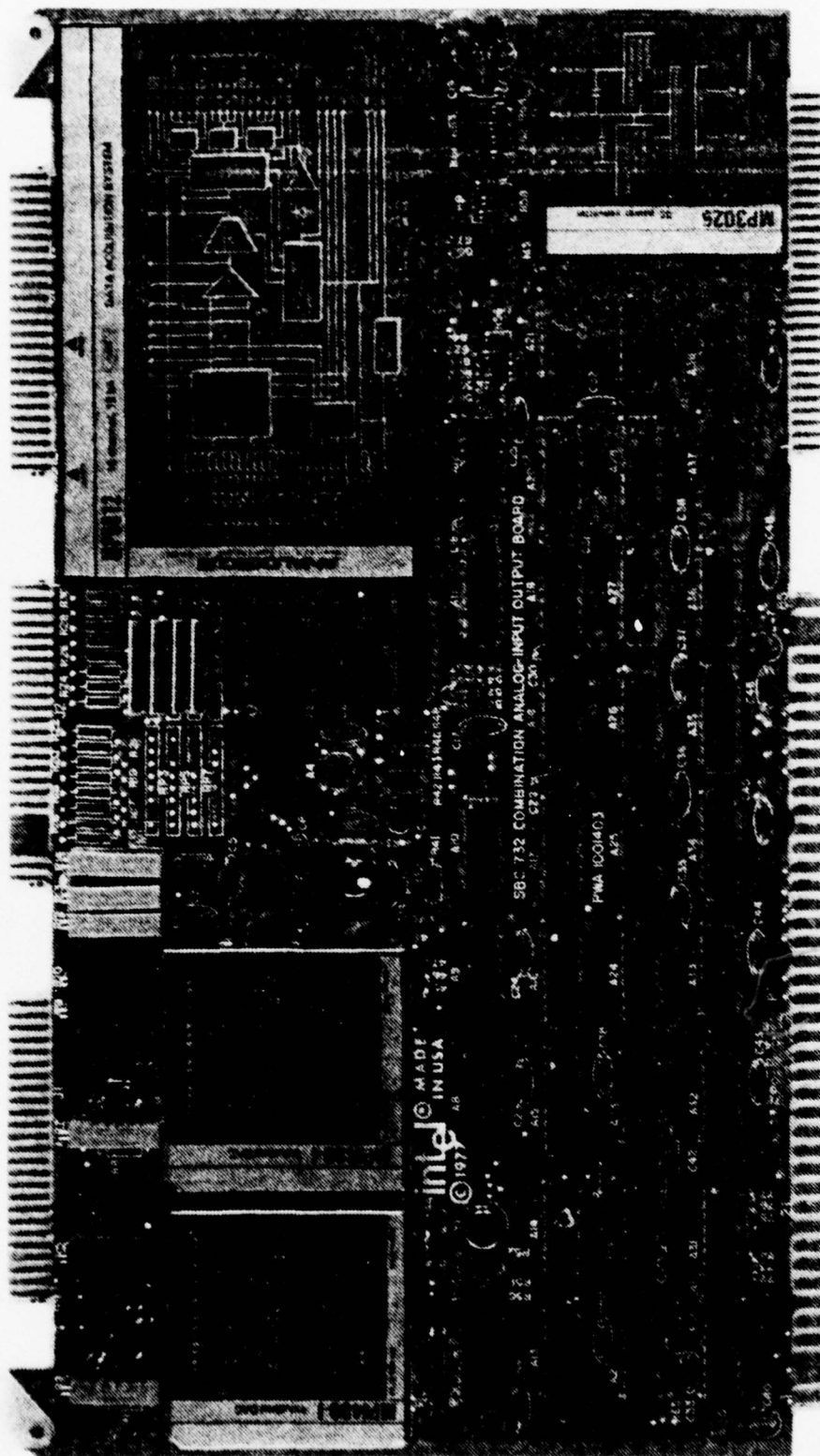


Figure 4. SBC 732 Combination Analog - Input/Output Board.

TABLE III
SBC-732 Specifications

Power Requirements:	$V_{CC} = +5V \pm 5\%$ $I_{CC} = 2.5A$ maximum
Physical Characteristics:	
Width:	34.48 cm (12.00 inches)
Depth:	17.15 cm (6.75 inches)
Thickness:	1.27 cm (.50 inches)
Weight:	567 gm (20 ounces)
Addressing:	Reserves a block of 16 contiguous memory locations relating to a jumper-selectable memory base address.
Analog Input:	
Number of Channels:	8 differential or 16 single-ended; expandable to 16 differential or 32 single-ended
Resolution:	12 bits (.025%), bipolar or unipolar
S/H Aperture Time	< 20 nanoseconds
S/H Uncertainty	5 nanoseconds
Overall Accuracy (25°C)	0.05% FSR \pm 1/2 LSB (Gain 2)
A/D Conversion Speed	28 KHZ
Throughput:	
Sample Rate (single channel)	17 KHZ
Channel-to-Channel Rate	16 KHZ
Analog Output:	
Number of Channels	Two, non-isolated
Resolution	12 bits, bipolar or unipolar (jumper selectable)
Voltage Output Characteristics:	
Output Ranges	+5V, +10V, -5V, -10V (jumper selectable)
Output Current	5 MA @ $\pm 10V$
Output Impedance	0.2 ohm

When writing, this mode accepts 7-bit parallel input data and formats the data word into serial format suitable for recording on the tape. When reading, a start command will cause one 8-bit character to be read and will present this data in parallel format at the output. Figure 5 shows the cassette tape recorder front panel and controls.

B. SOFTWARE PACKAGE

1. Tape File Format

The purpose of the file format is to control input and output data in a manner that will permit the determination of the quantity and the accuracy encountered in the Read/Write operations.

All data stored on the tape will be arranged in the file format illustrated in Figure 6. The three leading zeroes in each record of the file provide the means for a simple test to determine that the file type character is approaching. The next character after the leading zeroes is the file type character. This type of designation enables the specific identification of each file. Record type 1 refers to source data that was obtained from actual flights; on the other hand, record type 2 refers to source data derived from computer generated flight loads. The next character in a record is the record length, which indicates the quantity of bytes which comprise the remainder of the record (excluding the terminal checksum character). The record length character permits the writing of specific length Write/Read routines

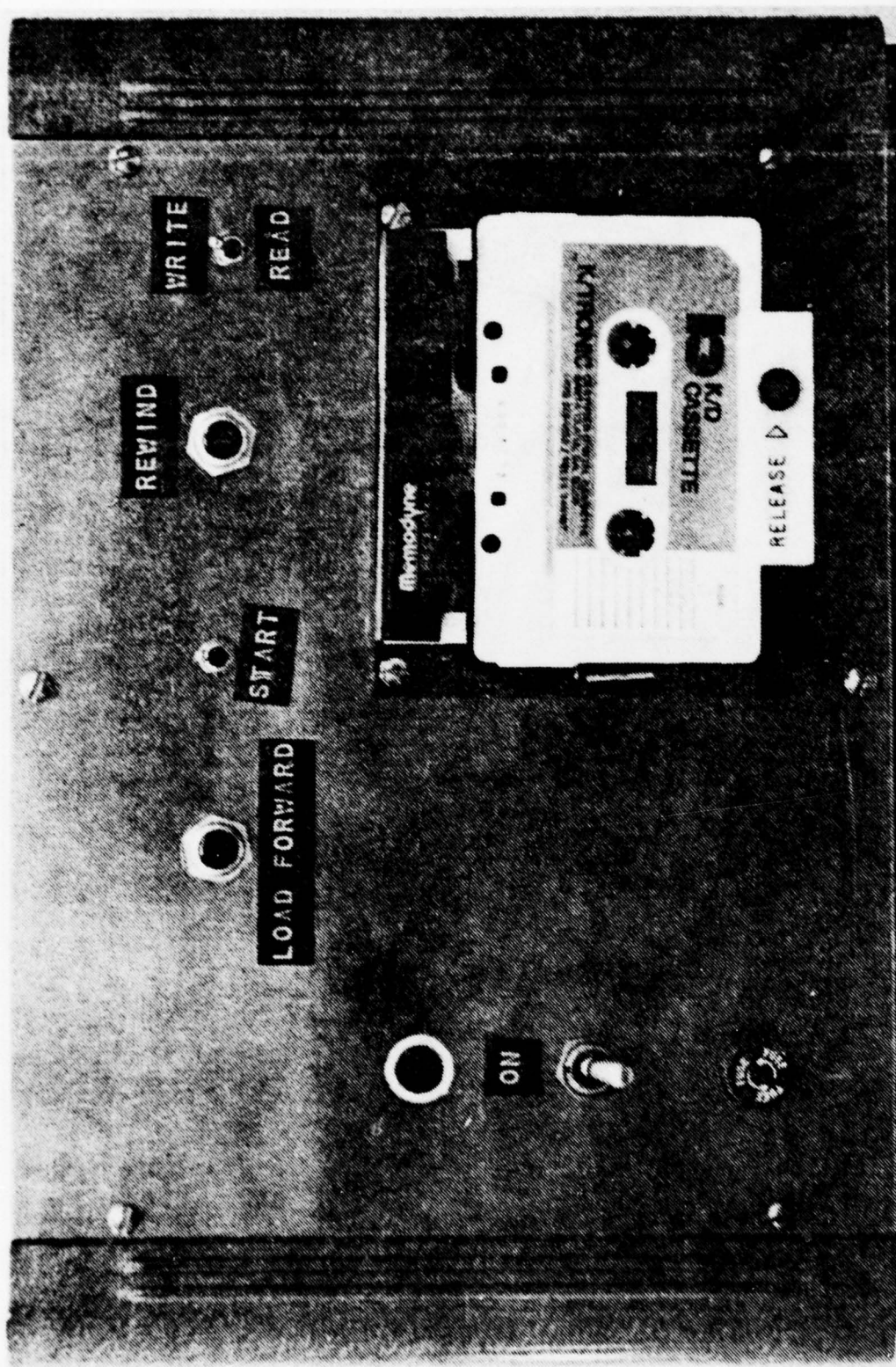


Figure 5. Memodyne Model 173 Cassette Recorder.

Leading Zeroes	Record Type	Record Length	Data	Checksum
000	01	81H	Julian Date Aircraft Type BUNO Configuration Gross Weight Mission	CS
000	02	94H	MTS Scale Factor Limit Load Cross Sectional Area Random Number Seed Strain Data Follows	CS
000	03	(00-FF)Hex	(Strain Gage Data)	CS
000	04	0DH	End of File	CS

Figure 6. Tape File Format.

and ensures that the proper quantity of data is transferred to/from the tape. The final character in a record is the checksum, which is a Modulo 257 sum of the data in the record between the file length character and the checksum itself. As the data are written on the tape, the checksum is calculated and then written at the end of each record to which they pertain. As the data are read from the tape, a new checksum is calculated, computed with the previously calculated checksum and, if an error exists, a CHECKSUM ERROR message is printed. This error would indicate that a difference exists between the data that were written on the tape and those which were read from the tape. A rewind and rerecord operation is then at the discretion of the operator.

2. The Write Program

The modularity of the design of the program permits a very simple conceptual construction of the Write main program. Subroutines are called to perform the "work" of the program, while the main program itself acts as the "manager." The operation of the main Write program is as follows:

The type of source load data, actual or simulated, determines whether record type 1 or 2 is accepted into RAM. The recorder head is moved onto the tape oxide in preparation to write. For each file, only three records are written on the tape. The tape is stopped after the fourth record is written. The system 80/10 returns to monitor for future control of the operation.

a. Write a Record Subroutine

The "write a record" subroutine (WRREC) writes the individual records on the tape. Operation of the subroutine is as follows: 1) The address in RAM that starts the record data chain is loaded into the H,L register pair. 2) The record type character is loaded into the D register. 3) The record length is loaded into the C register. 4) The subroutine is called. 5) A gap is put on tape for the physical separation of records. 6) The checksum is initiated. 7) The three leading zeroes, file type and file length are written on the tape. 8) One data byte is written on the tape. 9) The checksum is updated. 10) The record length counter is decremented. 11) If the record counter has not reached zero, indicating that all data have been transferred, the program continues looping through step 8. If the record counter has reached zero, all data have been transferred; and 12) the checksum is written on the tape. 13) Finally, another gap is put on the tape for further physical separation of files.

b. Write a Character Subroutine

This subroutine (WRCHAR) takes a byte of data from the accumulator and writes it on the tape. The operation of the "write a character" subroutine is as follows: 1) the tape recorder head is positioned near the tape oxide; 2) a write/start signal is sent to the tape recorder control port (the signal must be present for approximately one millisecond for the recorder to recognize it); 3) the status bit is sampled: if it is high, the recorder is still writing and the sampling

continues; when the status bit goes low, the recorder has completed the Write operation; and 4) a five millisecond delay is activated to ensure that the tape recorder is prepared to accept another write/start signal initiating the writing of the next data bit.

3. The Read Program

The Read main program also "manages" the Read sub-routines. The operation of the Read main program is as follows: 1) the tape recorder head is positioned near the tape oxide; 2) the RAM address at which the tape data will be stored is loaded into H,L register pair; 3) all records on the tape are read with each record type character being checked for the "end of file" record character (in this program, record type 4 indicates the end of the file). If the file type 4 is sensed, 4) the "end of file" message is printed and the 80/10 is returned to the calling program.

a. Read a Record Subroutine

This subroutine reads a record from the tape, stores the data in RAM, and outputs the data to the output device (CRT or TTY). The operation of the READ A RECORD subroutine is as follows: 1) the RAM storage address is loaded into the H,L register pair; 2) incoming data is checked and rejected until the input of the record leading zeroes; 3) the leading zeroes are noted, but rejected as data; 4) the record type character is accepted as the first bytes of significant data. If the record type is 4, the END OF FILE message is printed and 80/10 is returned to monitor. If the record type

is not 4, then 5) the record length character is accepted, indicating the length of data to be read from this record. 6) As each data byte is read, stored and output to CRT or TTY, a new checksum is calculated. After all data have been read, 7) the new checksum is compared with the previous checksum stored on tape, and if in error the CHECKSUM ERROR message is printed. If no error, 8) control is returned to the calling program.

b. Read a Character Subroutine

This subroutine takes a byte of data from the tape and moves it into the accumulator. The operation of the READ A CHARACTER subroutine is as follows: 1) the read/start signal is sent to the tape recorder control port for at least one millisecond; 2) the tape SYNC bit is sampled until it is high, indicating that the tape recorder has commenced reading; 3) the tape SYNC bit is sampled until it is low, indicating that the tape recorder has completed the Read operation; 4) a 5-millisecond delay is provided to ensure that the tape recorder is ready to accept the next read/start signal for the next character; 5) the byte of data is sent from the tape recorder output port to the accumulator; 6) control is returned to the calling program.

4. ADC and DAC Programming

a. General Description

This section illustrates and describes the command, status, and data formats for programming the ADC and DAC channels. A more complete description can be found in Ref. [2]. Also see Table IIIA for pin assignments.

TABLE IIIA
SBC 732 ANALOG OUTPUT/INPUT
PIN ASSIGNMENT

Edge Connector/ Pin Number	Function	EIA	Comments
J1/36	DAC 1: V_{out}	19	Analog Output
J1/39	DAC 1: Analog Rtn		
J1/42	DAC \emptyset : V_{out}	18	
J1/45	DAC \emptyset : Analog Rtn		
J2/4	Channel \emptyset	1	Analog Input
J2/6	8	9	
J2/8	1	2	
J2/10	9	10	
J2/12	2	3	
J2/14	10	11	
J2/16	3	4	
J2/18	11	12	
J2/20	4	5	
J2/22	12	14	
J2/24	5	6	
J2/26	13	13	
J2/28	6	7	
J2/30	14	16	
J2/32	7	8	
J2/34	15	17	
J2/3-33	Analog Rtn	21-25	
J2/39-45	Digital Common	1	
J2/40	Clock Out	2	
J2/42	Ext. Trigger In	3	
J2/44	EOC Trigger Out	4	
J2/46	EOS Status Out	5	
J2/48	Analog Return	6	

The system 80/10 communicates with SBC-732 through a sequence of Read and Write commands. Table IV lists the individual commands associated with the SBC-732 ADC and DAC's. These commands are addressed as specific memory locations relative to the memory base address F700H. If, for example, the memory base address is F700, the address $M + A$ implies the specific memory address F70A.

b. Read/Write Formats

The multiplier (MUX) address and gain format is shown in Figure 7. Bits 0-4 select the desired channel. Starting channel for a random ohms, bit 5 is ignored, and bits 6-7 select the programmable gain amplifier (PGA) input voltage gain. Table V lists programmable gain versus ADC full-scale range available to SBC-732 board.

The MUX address and gain are established by performing a Write to $M + 1$; the MUX address and gain may be verified by performing a Read of $M + 1$.

c. Command Register Format

The command register, which is associated entirely (either directly or indirectly) with the A/D conversion process, is loaded by a Write command to $M + 0$. Bit 0 must be set before the A/D conversion can occur. The command register format is shown in Figure 8.

d. Status Register Format

The contents of the status register, which contains the status of the ADC and the function associated with the A/D conversion process, are assessed by a Read command

TABLE IV
SBC - 732
MEMORY ADDRESS ASSIGNMENTS

MEMORY ADDRESS	COMMAND	FUNCTION
M+0	Write	Load Command Register
M+0	Read	Read Status Register
M+1	Write	Load MUX Address Register and Gain Register
M+1	Read	Read MUX Address Register and Gain Register
M+2	Write	Load Last Channel Register
M+3	Write	Clear Interrupts
M+4	Read	Read Lower Byte of ADC Value
M+5	Read	Read Upper Byte of ADC Value
M+8	Write	Output Lower Byte for DAC0 to Hold Register (HR)
M+9	Write	Output Upper Byte to DAC0 (DAC0 → HR automatically)
M+A	Write	Output Lower Byte for DAC1 to Hold Register (HR)
M+B	Write	Output Upper Byte to DAC1 (DAC1 → HR automatically)

TABLE V

Programmable Gain Vs. ADC Full-Scale Range

ADC FULL-SCALE RANGE				GAIN
+5V	+10V	$\pm 5V$	$\pm 10V$	
+5V	+10V	$\pm 5V$	$\pm 10V$	X1
+2.5V	+5V	± 2.5	$\pm 5V$	X2
+1.25V	+2.5V	± 1.25	± 2.5	X4
+0.625V	+1.25V	$\pm 0.625V$	± 1.25	X8

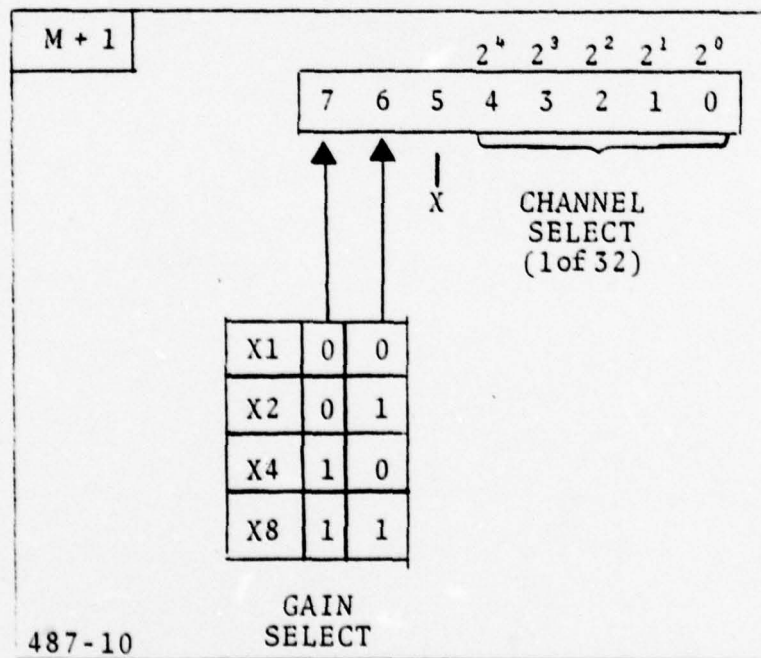


Figure 7. MUX Address and Gain Format.

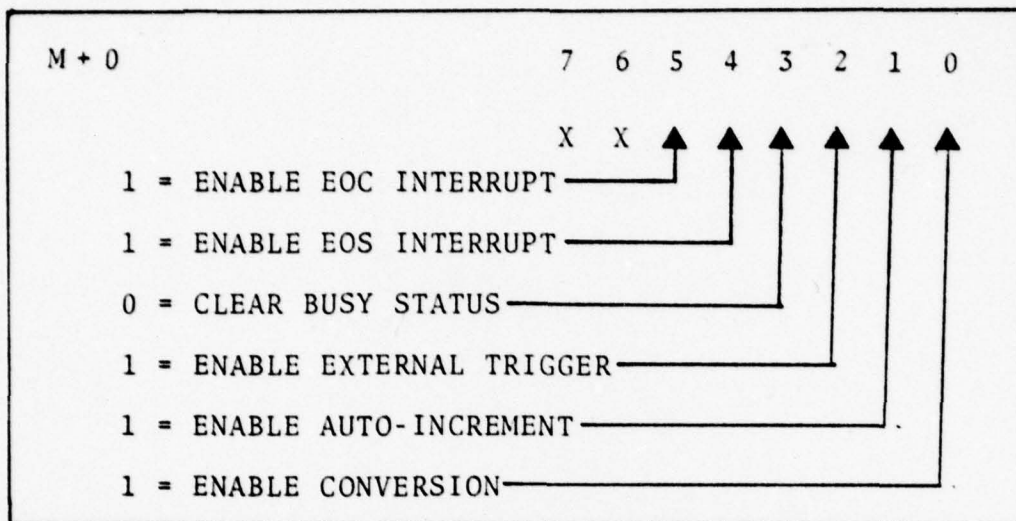


Figure 8. Command Register Format.

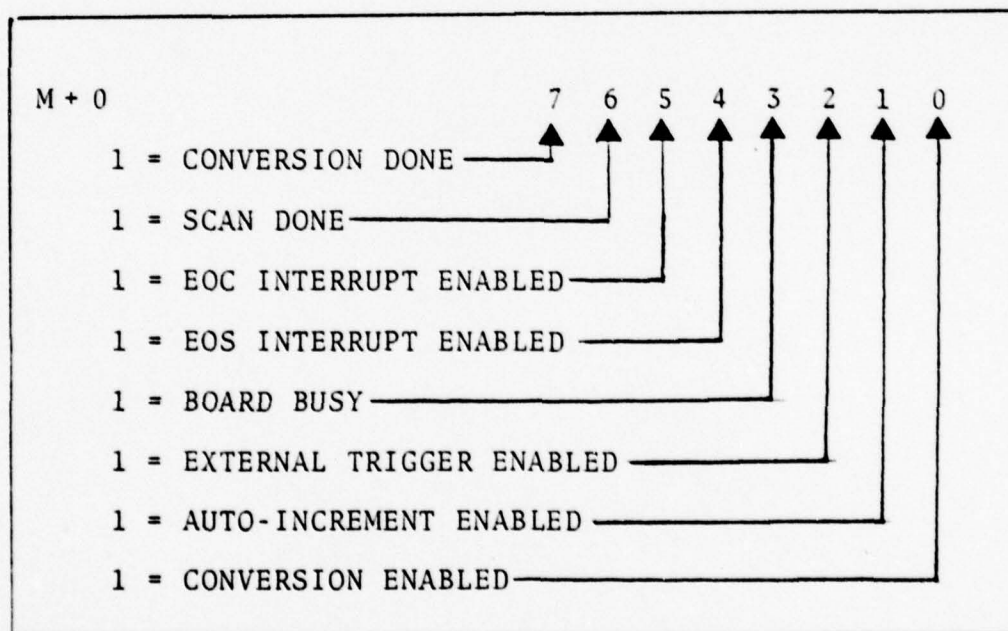


Figure 9. Status Register Format.

to M+0. As shown in Figure 9, bits 0 through 5 essentially verify the last command word written into M+0. Bit 3 (BOARD BUS), however, has a special function. The first time a Read command to M+0 is performed after the busy status bit is cleared by a Write to M+0, the BOARD BUSY bit will be read as a "0". Each time thereafter that the status register is read, the BOARD BUSY bit will be returned true. This function is useful for multiprocessor systems in which two or more processors are sharing the SBC-732. The BOARD BUSY bit can be cleared only by Write command to M+0 with bit 3 clear. Bits 6 and 7 are used primarily in non-interrupt driven programs to determine when valid ADC data are ready to be read (CONVERSION DONE).

e. ADC Data

After the A/D conversion is complete, the data word is obtained by a Read command to M+4 and a Read command to M+5, as shown in Figure 10. M+4 contains the ADC bits 0 through 3 and M+5 contains ADC bits 4 through 11.

ASSEMBLY LANGUAGE:

```
ADDATA EQU BASE + 4
LHLD ADDATA ; LOAD HL REG WITH
ADC DATA WORD
```

f. DAC Data

No program control other than the output data word is required by the two DAC channels. For DAC0, a Write command must be given first to M+8 to load the low byte into

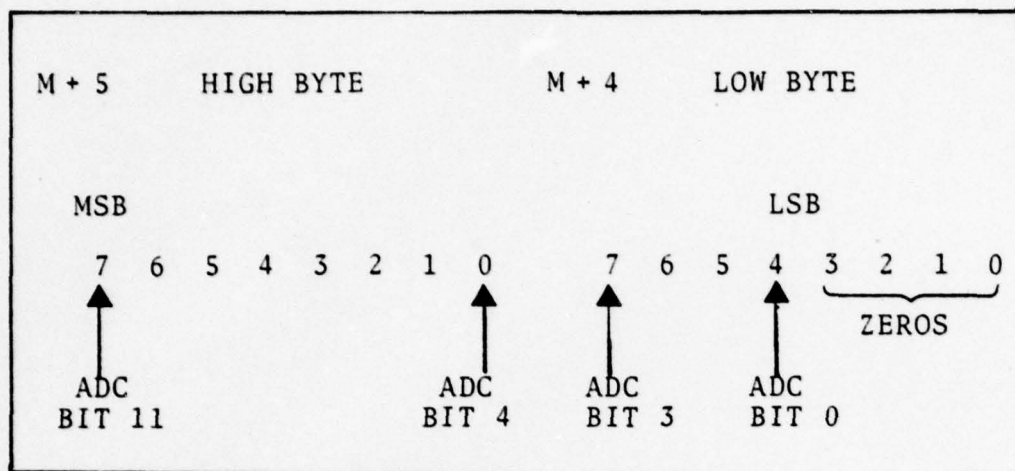


Figure 10. ADC Data Format.

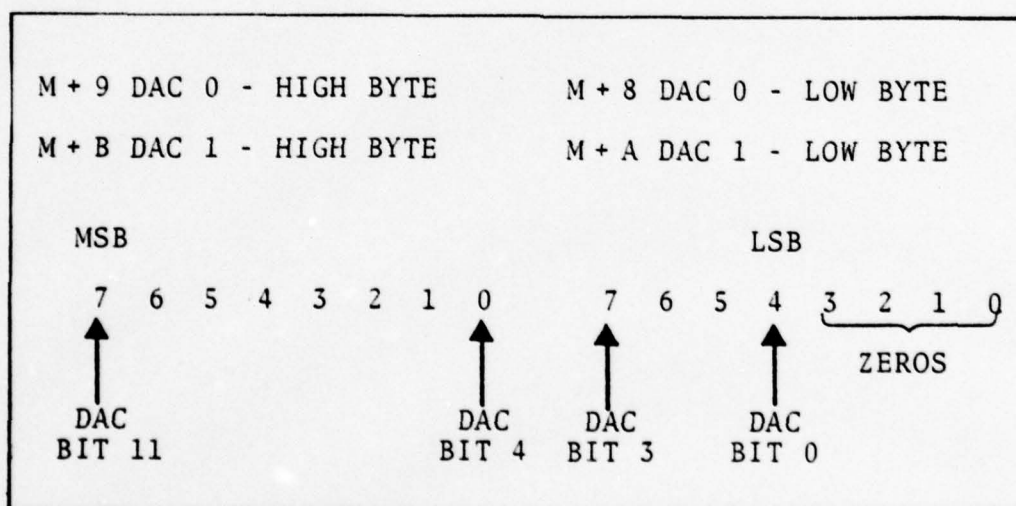


Figure 11. DAC Data Format.

the Hold Register, and the Hold Register maintains the four least significant bits valid at the DAC0 input; then a Write command to M+9 presents the high byte to the DAC0 input. DAC0 automatically makes the conversion of the input data when the Write command to M+9 occurs.

The operation of DAC1 is identical to that of DAC0 except M+A is used for the low byte and M+B is used for the high byte. Figure 11 illustrates the DAC data format.

ASSEMBLY LANGUAGE:

```

DACPFS EQU 0FFF0H; DAC POSITIVE FULL SCALE
LXI    H, DACPFS ; LOAD HL  FFF0H
CALL   WRDAC0    ;
      .
      .
      .
WRDAC0: SHLD  DAC0 ; OUTPUTS POSITIVE FULL
                        SCALE
                        RET                (FFF0)

```

III. RANDOM LOAD TESTING

This section deals with the load sequence randomization technique developed by Lt. John Scott Atkinson, Jr., [Ref. 3]. Fortran arithmetic declarations and manipulations required for 8080 CPU compatibility; the communications interface program called "HEXLINK" which links the MDS-800 to the IBM-360 via telephone line; and the preparation of paper tape of random load data using modification of DUMP program written by Digital Research for CP/M.

A. LOAD SEQUENCE RANDOMIZATION TECHNIQUE

The load sequence randomization technique uses the computer library subroutine RANDU to place 10 percent of the MIL-SPEC 8866, spectrum A positive loads [Table VI] in a random order. Each of the positive loads is paired with a minimum load of 11 percent limit load, representing 1-g flight. During the randomization, each load has an equal probability of selection. A counter restricts the number of times a value is selected to the number of occurrences of the particular load level in MIL spectrum A.

B. RANDOM LOAD ACCURACY AND FORMAT

The accuracy and format of the random loads were dictated by the 16-bit double precision accuracy of the 8080A CPU and the ADC and DAC 12-bit data word format.

TABLE VI
FREQUENCY OF MANEUVER LOADS
Number of Times per Thousand Hours
that Load Factor is Experienced

<u>Percent of Maximum (Positive) Symmetrical Limit Load Factor</u>	<u>Flight Maneuver Load Spectrum A</u>
35	17,000
45	9,500
55	6,500
65	4,500
75	2,500
85	1,360
95	440
105	150
115	40
125	<u>16</u>
Total	42,006

1. 8080A CPU Double Precision Accuracy

Since the 8080A CPU can handle 16-bit binary data, the loads generated in Fortran algorithm were limited to 16-bit accuracy by simply declaring them INTEGER *2.

2. ADC and DAC Data Word Format

The ADC and DAC data word is formed using double precision (2 bytes), referred to as the high byte and the low byte. The 12 most significant bits are used in the conversion process, thus leaving the least significant 4 bits of the low byte with zeroes. This nibble was used to indicate the

designated channel (0-15). The ADC and DAC word is shown in figure 12.

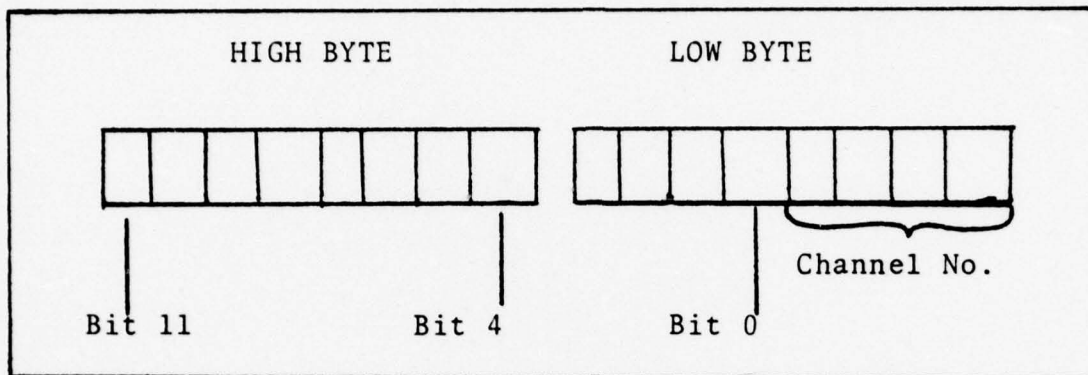


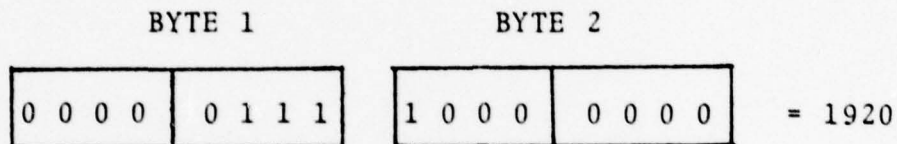
Figure 12. ADC and DAC Data Word.

3. Conversion of Loads to Voltages

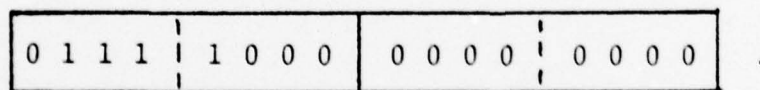
The next step in the analysis was to correlate a load generated with a corresponding voltage. The MTS requires ± 10 volts to operate its four selectable load ranges. The load ranges are: $\pm 10,000$, $\pm 20,000$, $\pm 50,000$, and $\pm 100,000$ lbs. The range is selected by estimating the maximum stress expected to occur. For example, if the 20,000 lb. range is selected and +10 volts is applied, then the MTS will produce a 20,000 lb. load. Using 12 bits, the largest number that can be represented is $16 \times 16 \times 16 = 4096$ parts. This means that if the voltage range is ± 10 volts, then there would be 4096 incremental steps between the voltage limits. Since our investigation will deal with positive loads only (0 to +10 volts), the incremental range is now 2048 steps. In other words, there are 204.8 steps per volt in the 0 - 10 volt range.

Now, to determine the number of steps required for a specific voltage supply, multiply the voltage by 204.8 steps/volt: for example, 9.375 volts is equivalent to 1920 steps.

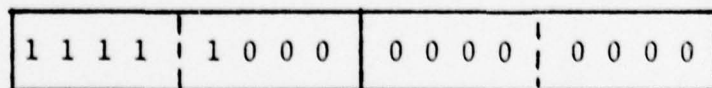
Next, the number of steps is represented in a 16-bit pattern. Continuing with our example, the 1920 steps have the following 16-bit pattern.



Since the analog converter data word used the three most significant nibbles of the combination of Byte 1 and Byte 2, the above 16-bit pattern is shifted 4 bits to the left. This is accomplished by multiplying the above 16-bit pattern by 16. The 16-bit pattern now is as follows:



The representation of 0 volts by the ADC or DAC is 8000H or (1000 0000 0000 0000) in 16-bit binary pattern. This means that the least value of the most significant nibble is eight (1000). This means that 1 is added to the most significant bit, giving the following 16-bit pattern:



The same results can be achieved by adding 32,678 to the 16-bit pattern.

The Hexdecimal representation of the above 16-bit binary number is F800. In summary, if F800 is outputted via DAC channel, it will produce a voltage of 9.375 volts, which corresponds to a load of 18,750 lbs. on the 20,000 lb. MTS load range.

4. Example

Suppose the critical stress analysis of a specimen with 0.9 in.² cross-sectional area had a limit load of 14,000 PSI. The largest load in the spectrum will be

$$1.25 \times 14,000 \times .9 = 15,750.$$

Selecting the 20,000 lb. load range and multiplying by 10 volts,

$$\frac{15,750 \text{ lbs.}}{20,000 \text{ lbs.}} \times 10 \text{ volts} = 7.875 \text{ volts.}$$

In other words, 7.875 volts is equivalent to 15,750 lb. load, when the 20,000 lb. range is selected on the MTS.

To determine the number of steps from zero, multiply by 204.8 steps/volt, and add 2048 (difference between 8000H and 0000H. The result is 3660.8 steps with respect to 0000H. Converting to hexadecimal gives ^hEaDX, where X designates channel number.

Now that the random load data have been created and stored in a vector, a technique for transmitting the data to the microcomputer development system needs to be considered.

C. COMMUNICATIONS LINK BETWEEN IBM-360
AND MDS-800 VIA TELEPHONE LINE

1. CP/CMS Control Program-67/Cambridge Monitor System

The CP-67/CMS time-sharing system consists of two independent components: the control program (CP67, or CP for short) and the Cambridge monitor system (CMS). The control program creates the time-sharing part of the system to allow many users to access the computer simultaneously. The Cambridge Monitor System provides the conversational part of the system to allow a user to monitor his work from a remote terminal.

CMS gives the user a full range of capabilities-- creating and managing files, compiling and executing problem programs, and debugging, using only a remote terminal. For a complete description of CP/CMS capabilities, see references [4] and [5].

2. MDS-800 Microcomputer Development System

In its basic configuration, the INTEL MDS-800 Microcomputer Development System consists of a CPU, 16K RAM, peripheral interface controller, front panel controller, power supply and enclosure. The MDS was directly connected to the following peripheral devices: CRT and keyboard console, high speed line printer, standard teletype with paper tape reader and punch [Ref. 6].

3. Disk Operating System

The INTEL Disk Operating System (DOS) consists of three major components, a dual floppy disk drive unit, a disk controller, and the DOS support software.

Each 7.5 inch diameter floppy disk (diskette) had a capacity of 256K bytes of semi-random access storage. With the dual drive, over 0.5 million bytes of data, programs, or other information could be assessed with relative ease and moderate speed.

The software support package offered by Digital Research was chosen as the operating system. CP/M consists of several utility routines in addition to the Basic Disk Operating System (BDOS). These routines allow the user to form and edit disk files, programs, or data files, and to assemble and load assembly language programs, and provide a powerful debug routine. A more complete description of the CP/M BDOS is contained in Ref. [7].

4. Hexlink Program

Assembly language was used in the construction of the communications interface program "LINK." "LINK" was developed by Lt. Mack T. Elliott, USN [Ref. 8] to interface the MDS-800 (and Model 40 printer) with CP/CMS through a 1200-band telephone line. Both the line and the printer are driven by an 8251 USARTS incorporated in a SBC-534 I/O board. LINK operates in one of three modes:

- a. Direct link up mode
- b. Transmit file mode
- c. Receive file mode.

Our discussions will be limited to the Receive File Mode. The Receive File Mode issues all CP/CMS commands to effect the transfer of an entire P-Disk File to the floppy disk.

It was necessary to modify "LINK" in its original form to include special filtering techniques of characters so that load data would be in hexadecimal format. This means that all characters other than (0-9, A-F) were excluded. It was still necessary to employ additional filtering because of the CP/CMS DumpF command output format.

5. CP/CMS DumpF Command

CP/CMS DumpF command types the contents of all or part of a specified file in hexadecimal format. The output format is as follows:

```
RECORD      1  LENGTH = 512
(Hexidecimal Data)
```

The additional filtering is required because the E, C, and D in the word "Record" would slip through the filter, since these letters are between A and F. These letters would pair up and pass as valid data, i.e., E and C would form together the hexadecimal byte value of "EC." Now, taking the entire first line, the following characters would be accepted as valid data: EC D1, E5 12, followed by normal data. It can be readily seen that when the record count becomes double digit, i.e., 10 or greater, the number of characters to pass through the existing filter system will be odd, thus allowing an invalid character to form with valid data. This has the effect of shifting valid data to the right one character, completely changing the meaning of the data. The solution to this problem was to check for character R, then filter out

all characters until carriage return line feed was sensed, which comes after the 512 in the above example. This ensures that all characters that pass through are valid data. With the above filtering techniques, the system is ready to pass data from CP/CMS to the Microcomputer Development System via telephone line. The procedures are outlined below:

```
USER      : CONTROL R
HEXLINK   : CMS  FILENAME FILETYPE
USER      : FILE  FT09F001
HEXLINK   : DISK:  FILENAME FILETYPE
USER      : A : DATUM1  HEX
HEXLINK   : DUMPF
           RECEIVING
           .
           .
           .
HEXLINK > TRANSMISSION COMPLETE
           0200 RECORDS TRANSMITTED
```

D. PUNCHING LOAD DATA ON PAPER TAPE

1. CP/M

CP/M is a monitor control program for microcomputer system development which uses IBM compatible flexible disks for back-up storage. Using a computer mainframe based upon Intel's 8080 microprocessor, CP/M provides a general environment for program construction, storage, and editing, along with assembly and program check-out functions [Ref. 9].

2. CP/M Dump Command Modification (Dump1)

The Dump program was developed by Digital Research Corp. and is compatible with the CP/M system. Dump types the contents of the disk file at the console in hexadecimal form. The file contents are listed 16 bytes at a time with the absolute byte address listed to the left of each line in hexadecimal.

Dumpl is a modification to DUMP program that types into memory, beginning at address 4400H, the contents of the disk file. This modification was necessary in order to punch a paper tape of data.

3. Punching Paper Tape Command

The MDS-800 monitor commands, specifically W4400, F700, will punch a paper tape beginning at address 4400H and terminating at F700H. The tape will be punched in standard INTEL HEX FORMAT. The data generated on the IBM-360/67 is now on paper tape in proper INTEL format, ready to be read into the system 80/10 via TTY.

IV. SYSTEM OPERATION

A. SUBROUTINES

Appendix C contains a complete source listing of all subroutines, including a branch table with PROM and memory address locations. Selected subroutines will be discussed in detail because of complexity and function. The less complex subroutines are self-explanatory. Other subroutines, such as WRCHAR, WRREC, RDCHAR, and RDREC, are covered in a separate section.

The following subroutines will be discussed in detail: VALDT, SGLCHN, HEADING, DSFILE, and RAMP.

1. VALDT Subroutine

The VALDT subroutine is designed to determine whether an event is strain significant or not. VALDT was intended to be used in conjunction with SGLCHN and STORE subroutines.

The change in magnitude of a signal is the first indication that an event may be strain significant.

The sign of the slope of the change is determined and compared with the sign of the slope of the previous change. Any change in sign will identify a peak or a valley in the analog signal and is further indication that a strain-significant event may have occurred. Furthermore, to be significant, the value of the strain reading must be above a predetermined positive threshold or below a predetermined negative threshold.

2. SGLCHN Subroutine

The SGLCHN subroutine uses the variables FSTCHN and GAIN to define the channel and gain to be used in the conversion of the analog input. The logic is similar to the subroutine, RANCHN, supplied by the manufacturer; however, SGLCHN uses VALDT subroutine to determine strain significant data. This routine converts a single channel at a time, although the algorithm could very easily be expanded to include a series of channels.

The mechanics of the software that accomplished this task are based on a four-element vector. They are as follows:

- X : the current value of the signal on the designated channel
- XLST: the previous value of the signal on the channel designated
- SIGN: the sign of the last change of the signal on the channel designated (00H means positive slope; 01H means negative slope)
- FLAG: an indicator showing the status of the last strain-significant event. 00H means within the threshold, non-zero means outside the threshold.

Once a signal has been determined to be a strain-significant event, it is identified by channel number and stored in RAM. If an event fills the last storage location in RAM, the recording procedure is initiated. The block of 256 bytes containing the data words is transferred, byte-by-byte, to the recorder and written on the magnetic tape. Upon completion of the transfer, the RAM is free to be refilled.

3. HEADING Subroutine

The HEADING subroutine is designed to format the heading information that is to be written on cassette tape. The particular heading information depends on whether the load data were computer generated or obtained from actual aircraft flights. The HEADING subroutine is used in ADCCHN, ADCMXM and MASTER Executive routines. The operator is guided through the subroutine by system prompt output on the (TTY). The first prompt the operator sees is as follows:

"LOAD DATA - ACTUAL OR SIMULATED A/S"

The operator now selects actual (A) or simulated (S) load data. If the operator selects "S", then the system will respond with the following prompts:

"MTS SCALE FACTOR (ENTER 5-DIGITS + SB)"

The MTS SCALE FACTOR refers to the four load ranges ($\pm 10,000$; $\pm 20,000$; $\pm 50,000$; $\pm 100,000$) on the MTS. The operator enters the appropriate five digits. If 100,000 scale is used, the operator would enter 99999.

"LIMIT LOAD (ENTER 5-DIGITS)"

The LIMIT LOAD refers to the design load derived from stress analysis. The operator enters five digits plus a space bar.

"CROSS SECT AREA (ENTER 3-DIGITS + DP)"

The CROSS SECT AREA refers to the cross-sectional area of the test specimen at the point of investigation.

The operator can enter any combination of three digits and a decimal point, plus a space bar.

"RANDOM NUMBER SEED (ENTER 6 DIGITS)"

The RANDOM NUMBER SEED refers to the seed used in the load sequence randomization technique. The operator enters six digits plus a space bar.

Had the operator declared the load data as actual, then the system would display the following messages:

"JULIAN DATE (ENTER 4 DIGITS + SB)"

The operator enters three digits representing the number of days that have expired since the beginning of the year plus the last digit of the year; example, 1358 (135th day, 1978). The operator types space bar for next prompt.

"AIRCRAFT TYPE (ENTER 4 CHARS + SB)"

The AIRCRAFT TYPE refers to the type of aircraft the data were recorded from; for example, A-7E, F-4J, EA-6B, etc. The operator enters aircraft type plus space bar.

"BUNO (ENTER 6 DIGITS + SB)"

The BUNO refers to the Bureau number of the aircraft. The operator enters the six-digit Bureau number plus a space bar.

"CONFIGURATION (ENTER 1-LETTER A, B, C OR D)"

The CONFIGURATION refers to the external stores loading. The letters A through D are defined by the user. For example, the following definitions might be used:

"A" BASIC AIRCRAFT NO STORES
"B" CONF. "A" PLUS CENTERLINE STORE
"C" CONF. "B" PLUS INBOARD WING STORES
"D" CONF. "C" PLUS OUTBOARD WING STORES.

The operator enters the appropriate letter plus a space bar.

"GROSS WEIGHT (ENTER 5 DIGITS + SB)"

The GROSS WEIGHT refers to the aircraft's appropriate gross weight. The operator enters five digits plus space bar.

"MISSION (ENTER 1-DIGIT 1, 2, 3, 4 or 5)"

The MISSION is user defined. For example, the user may wish to define "1" as Air Combat or "2" as Close Air Support or "3" as Point Intercept, etc. The operator enters the appropriate digits plus a space bar.

The system will not write the above heading information on the cassette tape. Once all the heading information is recorded, the tape is stopped and the system returns to the calling routine.

4. Display File Subroutine (DSFILE)

The DSFILE subroutine is designed to output each record to the teletype in a specified format. It is designed to read each record of a file from a cassette tape and output the record on the TTY in a specified format. The output varies slightly, depending on whether the load data were actual, or computer generated. The operator is guided through the routine by prompts at the console.

We now consider the two different output formats. If the load data were computer generated, then the following output will be displayed:

SIMULATED LOAD DATA

MTS SCALE FACTOR : 20000

LIMIT LOAD : 30000

CROSS SECT AREA : .500

RANDOM NUMBER SEED :000583

STRAIN DATA FOLLOWS:

The system will now prompt the operator with the following:

HARD COPY STRAIN DATA (Y/N)

The operator now has the choice of typing out the converted data or not. If "Y" is depressed, then the converted data will be displayed in blocks of 256 bytes. Each block of data will have eight columns with 16 rows. Sample output follows:

HARD COPY STRAIN DATA (Y/N)Y

E4F8	03B8	7018	02B8	7018	03B8	7018	0048
7018	0138	7018	0AB8	7018	02B8	7018	0A38
7018	03B8	7018	03B8	7018	01B8	7018	02B8
7018	03B8	7018	02B8	7018	03B8	7018	03B8
71E8	03B8	7018	02B8	7018	03B8	7018	03B8
7018	03B8	7018	02B8	7018	03B8	7018	01B8
7018	03B8	7018	0C38	7018	02B8	7018	02B8
7018	03B8	7018	03B8	7018	01B8	7018	03B8
7018	02B8	7018	03B8	7018	1138	7018	03B8
7018	02B8	7018	03B8	7018	08B8	7018	03B8
7018	02B8	7018	02B8	7018	02B8	7018	03B8
7018	02B8	7018	03B8	7018	0038	7018	03B8
7018	03B8	7018	0E38	7018	01B8	7018	03B8
7018	0C38	7018	02B8	7018	03B8	7018	02B8
7018	02B8	7018	02B8	7018	02B8	7018	0AB8
7018	3138	7018	0E38	7018	02B8	7018	02

If the operator did not want a hard copy of the data, depressing any other key will dump the next 256 byte block a record type 3 data. This process will continue until control senses a record type 4, at which time the system will type "END OF FILE" on the TTY and return the user to the MAIN Executive routine.

Had the load data been from actual aircraft flights, then the operator would display the following information:

ACTUAL LOAD DATA

JULIAN DATE	: 1628
AIRCRAFT TYPE	: EA-6B
BUNO	: 132628
CONFIGURATION	: C
GROSS WEIGHT	: 52500
MISSION	: 1

The remainder of the routine would be exactly as described above. The display subroutine is used in the READ Executive routine.

5. RAMP Subroutine

The RAMP subroutine is designed to output a voltage via one of the DAC's corresponding to a particular load. The driver loads are stored in the random load buffer, which begins at memory location 4400H and extends to memory location 7F00H. The routine compares two successive loads, and increments or decrements the DAC output in steps of 0.00488 volts until the load values are equal. In between each incremental

step the system delays a total of 6 millisecc and checks the assigned channel for significant strain data. The 6 millisecc delay provides the DAC output with a constant slope at an average rate of one cycle per second.

When the two loads are equal, the system will relieve the next load and repeat the above process until a "1AH" byte, located at the end of the random data, is sensed. This byte signals the end of the data, and the system checks to see if it is to be repeated. The random load data can be repeated up to 16 times, providing approximately ten hours of continuous testing. At the completion of the specified number of runs, the routine prompts the operator "M>" by automatically returning to the MAIN Executive routine.

B. EXECUTIVE ROUTINES

The Executive routines integrate the entire software package by using various combinations of subroutines to perform specific tasks. There are seven Executive routines: MAIN, SORCLD, ADCCEN, ADCMXM, READ, LOAD, and MASTER. The following paragraphs will discuss in detail the function of each Executive routine.

1. MAIN Executive

The MAIN Executive routine is designed to function as an access routine to the other Executive routines and the system 80/10 monitor. It is assumed that the system has been powered up and the punch paper tape containing all the prompt

messages has been read in via paper tape reader. To gain access, the operator types in at the console "G3C40." The system will respond with "M>" followed by "CONTROL G FOR INSTRUCTIONS." Each time the MAIN Executive is accessed, the stack pointer is initialized and the DAC's are set to 0 volts. This last point is very important because, if it becomes necessary to "RESET" the system, both DAC's are driven to minus full scale. This could be disastrous if a test specimen were hooked up. Continuing, should the operator choose to enter CONTROL G at the console, the following prompt would be displayed:

CONTROL A - ENTER ADCCHN EXECUTIVE
CONTROL B - ENTER ADCMXM EXECUTIVE
CONTROL C - ENTER SBC 80/10 MONITOR
CONTROL D - EXIT EXECUTIVE ROUTINE
CONTROL E - ENTER MASTER EXECUTIVE
CONTROL G - INSTRUCTIONS
CONTROL L - ENTER LOAD EXECUTIVE
CONTROL R - ENTER READ EXECUTIVE
CONTROL S - ENTER SURCLD EXECUTIVE
Decimal Point = DP
Space Bar = SB.

The operator now has access to the entire system via the above instructions. At this point the operator may wish to access the system 80/10 monitor to read in a random load paper tape. The operator accesses the monitor system by a "CONTROL C." The system will respond with "." At this time, the

operator would load the punch paper tape containing the random load data into tape reader, switch mode switch to "KT" and depress the "R" key, followed by carriage return (CR). The system will begin to read in the punched paper tape of random load data beginning at memory location 4400. This process of reading in load data need be done only once, since the SORCLD Executive will permanently record load data on a master load tape. After reading the punched paper tape, the tape movement will cease and the system will display "." at the console. The operator should next depress the "RESET" button, located on the front panel of the system 80/10 microcomputer (Fig. 13). (Note: The machine should have hydraulic drive off prior to depressing reset button, since DAC's are driven to minus full scale.)

It is necessary to insert a special byte value (1AH) at F700H at this time to signal the end of load data and prevent the system from possibly interfering with the stack area.

To gain access back to the MAIN Executive, the operator types in at the console "G3C40." The system will respond with "M>" followed by "CONTROL G FOR INSTRUCTIONS."

If the operator has just used a paper tape containing random loads, the next Executive routine selected would be "SORCLD."

2. SORCLD Executive

The SORCLD Executive is designed to make a copy on magnetic tape of the data between memory locations 4400-F700 HEX. Before entering SORCLD, ensure that the tape is rewound,

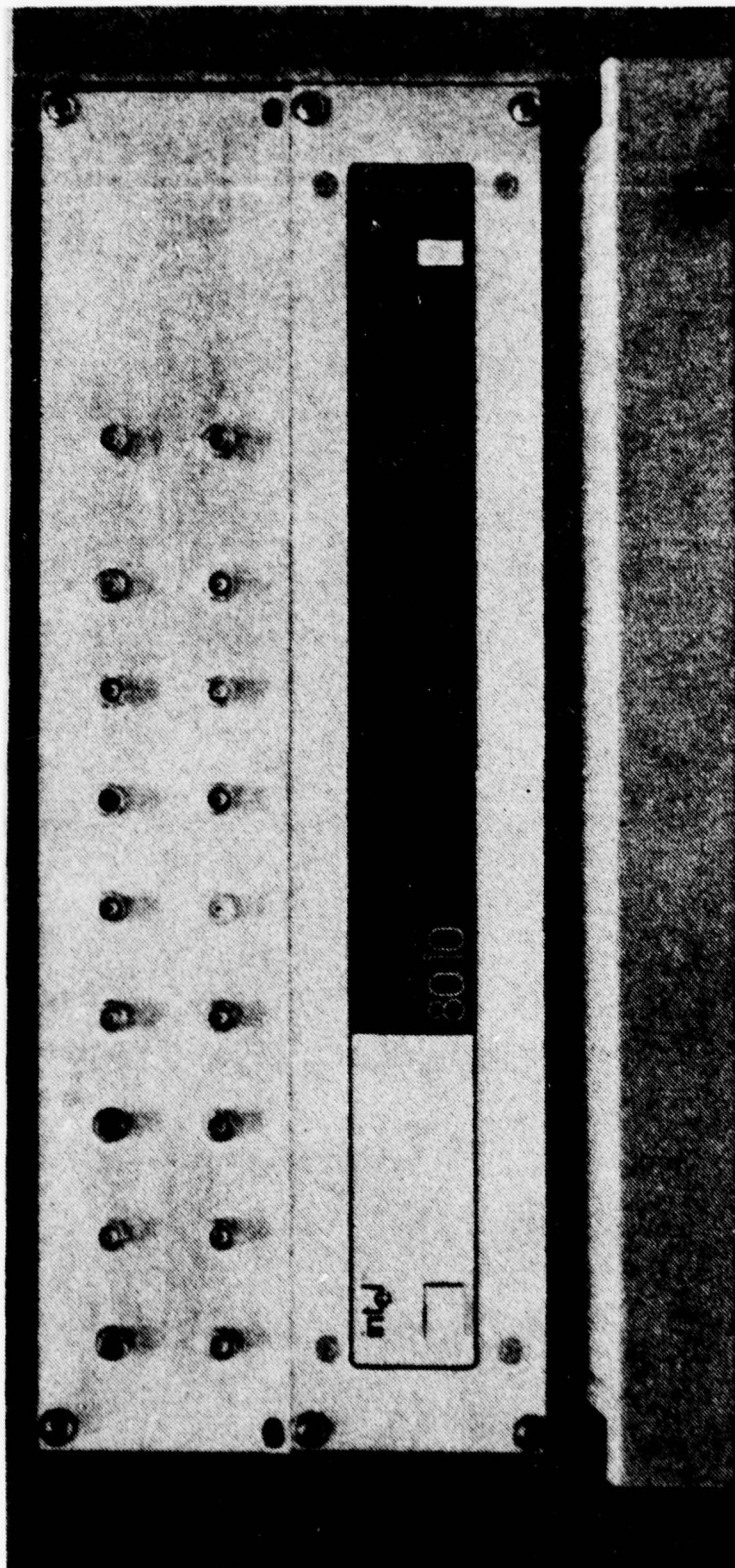


Figure 13. SBC-80/10 Front Panel with ADC and DAC BNC Connectors.

the "Write" head is engaged, and the Write/Read switch is in the "Write" mode. The routine is accessed from the MAIN Executive by holding down the "CONTROL" key and depressing the "S" key.

The cassette tape will begin to record load data located between 4400-7F00H. Upon completion, the system will display the following prompt and automatically return to the MAIN Executive:

LOAD DATA TRANSFER - COMPLETED
RELEASE WRITE HEAD
DEPRESS REWIND BUTTON
REMOVE LOAD DATA CASSETTE
ENGAGE WRITE HEAD
CHECK FOR M>.

"M>"

CONTROL G FOR INSTRUCTIONS

Had the random load data been recorded previously on magnetic tape, the operator could have elected to enter the LOAD Executive.

3. LOAD Executive

The LOAD Executive is designed to read random load data contained on cassette magnetic tape into memory, beginning at location 4400H.

Before entering the LOAD Executive, the operator should insure that the tape is rewound, the "Read head" is engaged, and the Write/Read switch is in the "Read" position. The LOAD Executive is accessed from the MAIN Executive by

CONTROL L command. The tape recorder will begin to read the random load data into memory. At the completion of this cycle, the system will indicate that the load data transfer is completed and will automatically return to the MAIN Executive. The prompt message is that given in the SORCLD Executive.

4. ADCCHN Executive

The ADCCHN Executive is designed to perform ADC on a selected channel at a 28 KHZ rate. The converted data is stored in a 256 byte buffer. When the buffer fills up, the data are dumped onto the cassette tape; thus freeing the buffer for more data. This process continues until the operator elects to terminate by entering a CONTROL D at the console, at which time the partially filled buffer is dumped onto cassette tape, the tape is stopped and the system is returned to the MAIN Executive routine.

The ADCCHN Executive is accessed from the MAIN Executive by entering a CONTROL A at the console. The system will respond with "A>" followed by LOAD DATA - ACTUAL OR SIMULATED (A/S)." Upon selecting "A" or "S" the operator will proceed through bookkeeping prompts as discussed under "Heading" subroutine. After heading information has been entered, the operator will be prompted with the following message, "SELECT CHANNEL NUMBER (0-F HEX)." The operator depresses the appropriate key, and the conversion process will begin. This routine is especially useful when testing and calibrating individual channels.

5. ADCMXM Executive

The ADCMXM Executive is designed to perform ADC on a selected channel. However, the only data that get stored in the data buffer, and eventually on cassette tape, are the maximum or minimum values of the converted data. The sub-routine VALDT provides the logic for obtaining maximums and minimums. The significant data bytes are stored in a memory data buffer until the buffer is filled, at which time the buffer is emptied onto cassette tape. This process continues until the operator enters CONTROL D at the console, at which time the partially filled buffer is emptied onto tape, and the tape is stopped. The routine will automatically return the user to the MAIN Executive routine.

6. MASTER Executive

The MASTER Executive is designed to drive the materials testing system (MTS) with a random load, and simultaneously monitor a selected channel for strain-significant events. The random load data used to drive the MTS are located beginning at memory location 4400H. [See Appendix B].

The MASTER Executive is accessed from the MAIN Executive by entering a CONTROL E at the console. The system will respond "E>" prompt, followed by "LOAD DATA - ACTUAL OR SIMULATED (A/S)." Following the heading prompts, the system will prompt the operator with "NUMBER OF RUNS ENTER - 1 DIGIT (0-H HEX)." The operator enters the number of times that the load data sequence is to be repeated, i.e., if the operator wishes to repeat the load sequence four (4) times, then enter a "4."

This capability enables the operator to run the load sequence 16 times, which, with the given number of loads (7552) will provide approximately 10 hours of continuous testing. The next system prompt is "SELECT CHANNEL NUMBER (0-F HEX)." The operator types in the channel number and the system will begin to convert data and writing peaks and valleys on the cassette tape. The system automatically returns to the MAIN Executive following the last cycle through the random load data.

7. READ Executive

The READ Executive is designed to read (or play back) a file created by the Write program. The information is displayed at the console. The operator has the choice of displaying record type 3 data or continuing. To gain access to the READ Executive from the MAIN Executive, the operator types a CONTROL R at the console. Prior to the CONTROL R command, the cassette tape should be rewound, the Read head engaged, and the Write/Read switch set to the Read position. The system will respond with "R," then immediately begin reading tape records. The record 1 or 2 type data are displayed directly. The record 3 data are dumped in 256 byte blocks into RAM, the tape is stopped, and the operator is prompted, "HARD COPY STRAIN DATA (Y/N)." If the operator should elect "Y," then the specific data loaded in the 256 byte RAM buffer are typed out at the console. If the operator enters "N," the next block of data is read from the tape into RAM. This process is continued until the END-OF-FILE is typed at the console and the system automatically returns to the MAIN Executive.

C. PRELIMINARY SYSTEM QUALIFICATION

This section discusses the preliminary qualification tests conducted on the data acquisition system, intended to exercise the device throughout its expected range of operation so that actual performance limitations, if any, may be determined.

1. DC Calibration

Accompanying the SBC-732 analog input/output model was a voltage calibration and scan test software package [Ref.]. This program consists of a three-step sequence which must be performed in the following order: (1) P&A offset adjustment, (2) ADC offset adjustment, and (3) ADC range adjustment. Following the calibration test, the ADC system was verified by applying standard precision DC voltage. The ± 10 volt range was chosen to coincide with the MTS machine operating range. The test also verified the subroutine associated with the ADCCHN Executive routine. For instance, 2 volts from a standard precision supply were read into the system and an interrogation of the memory showed that the system stored 99ADH, which corresponds to 2 volts.

2. Incremental DC Volt Step Test

This test was designed to verify the theoretical incremental voltage of each bit used in the conversion process. Using 12 bits for conversion, there are $(16 \times 16 \times 16 = 4096)$ incremental steps possible. This means, using the ± 10 volt range, that each incremental step is equivalent to 0.00488 volts. A standard precision voltage source was applied in steps of 5 millivolts. Changes in the bit pattern verified the theoretical predictions.

3. Sinusoidal Signal Reconstruction

This test was designed to evaluate the system's ability to accurately acquire, store, and reconstruct a sinusoidal signal with a known amplitude. The analog input signal was converted and recorded on cassette tape using the ADCCHN Executive routine. Recorded data were read into memory location beginning at 4400H and outputted via system DAC's to a strip chart recorder. The reconstruction signal was compared to the original signal and produced deviations of less than 1%.

4. Peak and Troughs Test

This test was designed to check the accuracy of the system to record only the peaks and valleys of a known sinusoidal signal. The amplitude of the sign wave was calibrated and measured. The peaks and valleys were reached on magnetic cassette tape using the ADCMXM Executive routine. The converted value from the tape was broken down into its voltage representation and compared with actual input voltage. For example, the converted value of FEE0H is equivalent to $(256 \times 15 + 16 \times 14 + 14 = 4078)$ steps, which is the same as 9.9121 volts. This voltage was then compared with strip chart value of 9.94 volts, which was well within the accuracies of the instrumentation.

5. Driver Test

This test was designed to exercise the entire system using the random loads generated on the IBM-360 and to drive the MTS machine using the RAMP subroutine. Some initial concern was experienced with the speed of the drive signal and

the response of the MTS machine when the output signals were attenuated by 10%. The RAMP rate was set to an average of .7 cycles per second; the first list results indicated that the driver voltage was being attenuated at both the peaks and troughs by about 0.5 volts on a 0 to 10 volt signal.

Further investigation revealed that because of the incremental step technique used to create the driver signal, it was necessary to hold the RAMP signal longer at the DAC output port before proceeding to the next incremental step. Test results indicated that it was necessary to hold the signal at the DAC output about 8 millisec before proceeding to the next step. The resulting acceptable driver rate was approximately .6 cycles per sec. This test verified the subroutines used in the MASTER Executive routine.

V. CONCLUSIONS AND RECOMMENDATIONS

Based upon the performance tests discussed in the preceding section, it is concluded that the system is executing its several modes of operation accurately and reliably. Furthermore, during the course of the software interface development, modifications were constantly made with the intent of improving the performance and operator interface. The modularity of the software package makes it versatile and easily adapted to future changing needs.

APPENDIX A

GLOSSARY

1. ASCII: American Standard Code for Information Interchange. This is a seven-bit-plus-parity code established by the American National Standards Institute to achieve compatibility between data services. Also called USASCII.
2. Assembly: A listing which contains both source code and machine code.
3. BIT: BInary digiT. A single unit of information in a binary word.
4. Buffer: A group of memory locations used to store specific data (input data, constants, output data, etc.).
5. Byte: An eight-BIT word which is processed as a single quantity.
6. CPU: Central Processing Unit. The area of the micro-processor which computes and sequences all logic and arithmetic functions.
7. CRT: Cathode Ray Tube - A television-like picture tube used in visual display terminals.
8. D/A: The inverse of the A/D process.
9. EPROM: Erasable/programmable read only memory.
10. I/O: Input/output.

11. K: A suffix which indicates a group of 1024 (2^{10}) items as in "4K of memory" meaning 4096 memory locations.
12. MUX: A multiplexing device.
13. Nibble: The upper or lower four BITS in one byte.
14. RAM: Random access memory. Volatile memory used for variable storage and data manipulation.
15. Register: A storage location located in the CPU.
16. ROM: Read only memory, non-volatile.
17. Sample and Hold: A device for sampling the amplitude of a signal at a given time and holding that amplitude.
18. Software: The program which resides in the U-P's memory.
19. Source code: The program written by the user.

APPENDIX B

MEMORY MAP

Beginning Address	Description	Ending Address
0000H	EPROM #1 SBC-80/10 Monitor	03FFH
0400H	EPROM #2 SBC-80/10 Monitor Subroutine	07FFH
0800H	EPROM #3 Subroutines	0BFFH
0600H	EPROM #4 Subroutines + Executive Routines	0FFFH
1000H	11K Uncommitted Memory	3BFFH
3C00H	RAM Reserved for Monitor	363FH
3C40H	Start Software Program	
	Buffer #3	
3C80H	Messages/Variables	42FFH
4300H	256 Byte Data Buffer	43FFH
4400H	15,104 Byte Random Load Buffer	7F00H
7F00H	Stack Buffer	7FFFH
F700H	Memory Based Address for SBC-732 Board	F77FH

APPENDIX C: SOURCE LISTINGS

```

*****
NEWTC VERSION 1.0  JUNE 1978
THE FOLLOWING ASSEMBLY LANGUAGE PROGRAM
INTERGRATES RANDOM LCAD TESTING ON
THE MATERIALS TESTING SYSTEM MACHINE
*****

```

```

3C40 31FF7F
3C40 C3860F
3C43

```

```

ORG 3C40H
LXI SP,7FFFH
JMP MAIN ;

```

```

*****

```

SYSTEM EQUATES

```

*****

```

8255 #2 PORT ADDRESSES

```

*****
PCRTA EQU 0E8H :8255 #2 PORTA
PCRTB EQU 0E9H :8255 #2 PORTB
PCRTC EQU 0EAH :8255 #2 PORTC
CCNTROL EQU 0EBH :8255 #2 CONTRCL WCRD
*****

```

```

00E8 = = =
00E9
00EA
00EB

```

CONSOLE (TTY)

```

*****
CRTD EQU 0ECH :CONSOLE DATA PCRT
CRTS EQU 0EDH :CONSOLE STATUS PCRT
RBR EQU 2 :RECEIVER READY BIT
TRDY EQU 1 :TRANSMITTER READY BIT
PRMBRT EQU 0800H :PROM 3 AND 4 BRANCH TABLE
*****

```

```

00EC = = = =
00ED
0002
0001
0800

```



```

3CAA 4149524352MSG1A: DB 'AIRCRAFT TYPE : '
3CBA 00000000 TYPE: DB 0,0,0,0
3CBE 0C0A DB CR,LF
3CC0 42554E4F20MSG1B: DB 'BUNC : '
3CL0 0C00000000SIDENO: DB 0,0,0,0,0,0,0
3CL6 0C0A DB CR,LF
3CL8 434F4E4649MSG1C: DB 'CONFIGURATION : '
3CE8 00 DB 0
3CES 0C0A DB CR,LF
3CEB 47524F535MSG1D: DB 'GROSS WEIGHT : '
3CF8 0C00000000GSWT: DB 0,0,0,0,0,0
3DCC 0C0A DB CR,LF
3DC2 4D49535349MSG1E: DB 'MISSION : '
3D12 0C DB 0
3D13 0C0A DB CR,LF
3D15 53494D554CMSG2: DB 'SIMULATED LOAD DATA W'
3D2A 0C0A0A DB CR,LF
3D2D 4D54532C53 DB 'MTS SCALE FACTOR : '
3D41 0C00000000SCALE: DB 0,0,0,0,0,0
3D46 0C0A DB CR,LF
3D48 4C494D4954 DB 'LIMIT LOAD : '
3D5C 0C00000000LIMT: DB 0,0,0,0,0,0
3D61 0C0A DB CR,LF
3D63 43524F5353 DB 'CROSS SECT AREA : '
3D77 0C000000 AREA: DB 0,0,0,0,0
3D7C 52414E444F DB CR,LF
3D51 0C00000000SEED: DB 'RANDOM NUMBER SEED : '
3D57 0C0A DB 0,0,0,0,0,C,C
3D59 5254524149 DB CR,LF
3D5C 0C0A0A DB 'STRAIN DATA FOLLOWS: '
3D80 454E44204FMSG3: DB CR,LF
3D80 DB 'END OF FILE',CR,LF

```

```

3D80 0C0A434845MSG4: DB CR,LF,'CHECKSUM ERROR',CR,LF,'$'
3D80 0C0A454E44MSG5: DB CR,LF,'END OF FILE',CR,LF,'$'
3D80 0C0A484152MSG5A: DB CR,LF,'HARD COPY STRAIN DATA (Y/A)','$'
3D80 4A554C4941MSG6: DB CR,LF,'JULIAN DATE ( ENTER 4 DIGITS + SB )',CR,LF,'$'
3D80 0C0A414952MSG7: DB CR,LF,'AIRCRAFT TYPE ( ENTER 4 CHARS + SB )',CR,LF,'$'
3D80 0C0A424554MSG8: DB CR,LF,'BUNC ( ENTER 6 DIGITS + SB )',CR,LF,'$'
3D80 0C0A434F4EMSG9: DB CR,LF,'CONFIGURATION ( ENTER 1-LETTER-A,B,C,OR'
3D80 4420282053 DB 'D + SB )',CR,LF,'$'
3D80 0C0A47524FMSG10: DB CR,LF,'GROSS WEIGHT ( ENTER 5 DIGITS + SB )',CR,LF,'$'
3D80 0C0A4D4953MSG11: DB CR,LF,'MISSION ( ENTER 1-DIGIT--1,2,3,4,CR 5'
3D80 2B20534229 DB '+ SB )',CR,LF,'$'

```



```

*****
; BUFFERS
;
*****

```

```

4300      ORG 4300H      256 ;DATA BUFFER
4300      BUFR: DS

44CC      ORG 4400H
4400      BUFR1: DS 15104 ;RANDOM LOAD DATA ELFFER
               ;4400H TC 7F00H

```

```

*****

```

```

0561      ORG 0561H

RCL0AD:   MVI 8,00H      ;INITIALIZE CHECK SUM
RCL1:     CALL RDCCHAR    ;READ FIRST BYTE
          CPI 00H
          JNZ RCL1 ;FIND FIRST NON ZERO CHAR

RCL2:     CALL RDCCHAR    ;
          CPI 00H
          JZ RDL2
          CPI 01H
          JZ RDL5
          CPI 02H
          JZ RDL5
          CPI 03H
          JZ RDL3
          CPI 04H
          CZ STOP ;STOP TAPE

056B      CCC00A
056E      FE00
057C      C46E05
0573      FE01
0575      CAB905
0578      FE02
057A      CAB905
057D      FE03
057F      CA9005
0582      FE04
0584      CC7A0A

```

0587 21FA3E	LXI H, MSGC ;
058A C07F08	CALL MSG ;
058D C3860F	JMP MAIN ;
0590 57	RDL3: MOV D, A ; STORE FILE TYPE
0591 C0C00A	CALL RDCHAR ; GET FILE LENGTH
0594 4F	MOV C, A ; STORE FILE LENGTH
0595 F5	PUSH P SW ;
0596 C0C00A	RDL4: CALL RDCHAR ; GET NEXT DATA EYTE
0599 77	MOV M, A ;
059A 8C	ADD B ;
059B 47	MOV B, A ;
059C 23	INX F ;
059D 0C	DCR C ;
059E C29605	JNZ RDL4 ;
05A1 C0C00A	CALL RDCHAR ; GET LAST CHAR
05A4 77	MOV M, A ;
05A5 8C	ADD B ;
05A6 47	MOV B, A ;
05A7 F1	POP P SW ;
05A8 4F	MOV C, A ;
05A9 C0C00A	CALL RDCHAR ;
05AC AE	XRA B ;
05AC EF	PUSH H ;
05AE 21BD3D	LXI H, MSG4 ;
05B1 C47F08	CNZ MSG ;
05B4 E1	POP H ;
05B5 23	INX H ;
05B6 C36105	JMP RLOAD ;
05B9 C0C00A	RDL5: CALL RDCHAR ; STORE RECORD LENGTH
05BC 4F	MOV C, A ;
05BD C0C00A	RDL6: CALL RDCHAR ;
05C0 0C	DCR C ;
05C1 C28D05	JNZ RDL6 ;
05C4 C36105	JMP RLOAD ;

SUBROUTINES

: PRGM 3 AND 4 BRANCH TABLE

```
0800      C36608      CONIN      FRMBRT  
0803      C37208      CONOUT  
0806      C37F08      MSG  
0809      C38A08      GAP  
080C      C39608      HEADNG  
080F      C3A609      OXICE  
0812      C3C709      WRCHAR  
0815      C3E509      WREC  
0818      C3I60A      RANCHN  
081B      C3D30A      LDATA  
081E      C3550A      LDRNDT  
0821      C37A0A      STOP  
0824      C37F0A      DUMP  
0827      C3910A      DELAYI  
082A      C3A50A      CLROX  
082C      C3C00A      RDCHAR  
083C      C3E30A      DSFILE  
0833      C3I80B      CRQUT  
0836      C39C0B      GOUGE  
0839      C3A70B      DACZRO  
083C      C3E00E  
  
          ;  
          #4  
          PROM  
  
083F      C3000C      SGLCHN  
0842      C3220C      VALCT  
0845      C3BF0C      STORF  
0848      C3E00C      RAMP  
084B      C3610D      WDACO  
084E      C3650D      WDACI  
0851      C3C50D      SORCLD  
0854      C3B30D      ADCCHN  
0857      C3320E      ADCMXM
```

085A C3B10E
085C C3C30E
086C C3E20E
086E C3860F

JMP READ ;
JMP LOAD ;
JMP MASTER ;
JMP MAIN ;

FUNCTION: CONIN
INPUTS : CHARACTER FROM CONSOLE
OUTPUTS : A - CHARACTER FROM CONSOLE
CALLS : CI
DESTRUCYS:
DESCRIPTION:

AWAITS UNTILL CHARACTER HAS BEEN ENTERED AT THE
CONSOLE AND THEN RETURNS CHARACTER VIA THE A
REGISTER TO THE CALLING ROUTINE.

CCIN:
PUSH B
PUSH D
PUSH H
CALL CI
ANI 7FH ; PARITY MASK
POP H
POP D
POP B
RET
;EXIT CONIN

0866 C3
0867 DE
0868 E5
0869 CDFDC3
086C E67F
086E E1
086F D1
087C C1
0871 C5

FUNCTION: CONOUT
INPUTS : C - CHARACTER TO OUTPUT TO CONSOLE
OUTPUTS : C - CHARACTER TO CONSOLE
CALLS : CO
DESTRUCYS:
DESCRIPTION:

WAITS FOR INPUT CHARACTER AND THEN
SENDS CHARACTER TO CONSOLE

CCNOUT:
PUSH PSW
PUSH B
PUSH D
PUSH H
MOV C, A

0872 F5
0873 C5
0874 D5
0875 E5
0876 4F

0877 CDFA03
087A E1
087B D1
087C C1
087D F1
087E C9

CALL CD
POP H
POP C
POP B
POP PSM
RET
:EXIT CONOUT

087F 7E
0880 FE24
0882 C8
0883 CD7208
0886 23
0887 C37F08

MSG: MOV A,M :CHAR TC ACCM
CPI :\$:CHECK FOR MESSAGE DEIMTER
RZ
CALL CONOUT :OUTPUT CHAR TO CONSOLE
INX H :INCREMENT MESSAGE PCOUNTER
JMP MSG :GET NEXT CHAR

088A FE
088B 3E20
088C D3EA
088F 3EEF
0891 CC510A
0894 F1
0895 C5

.....GAP: PUSH PSM :SAVE ACCM FLAGS
MVI A,20H :00100000
OUT PORTC :LWC/FWD BLANK TAPE
MVI A,OFFH :256MSEC DELAY
CALL DELAY1
POP PSM :RESTORE ACCM AND FLAGS
RET :EXIT GAP

FUNCTION: HEADNG
INPUT : A- CHARACTERS FROM CONSOLE
OUTPUT : CONIN CONOUT
CALLS :
DESTRCYS:
DESCRIPTION: INPUTS JULIAN DATE, AIRCRAFT TYPE, EUNO,
CONFIGURATION, GROSS WEIGHT, AND MISSICN.

0896 215440
0899 CL7F08

.....HEADNG: H,MSGE :DISPLAY SOURCE CF LCAD DATA
LXI
CALL MSG :

085C	219A42	LXI	TYPOTA	:PCINT TO TYPE DATA BUFFER
085F	CC6608	CALL	CONIN	:
08A2	CC7208	CALL	CONOUT	:
08A5	229A42	STA	TYPCTA	:STORE TYPE DATA
08A8	FE41	CPI	A	:A-ACTUAL S-SIMULATED
08AA	C24409	JNZ	REC7	:IF SIMULATED JMP
08AD	CC5C08	CALL	CROUT	:OUTPUT CR LF
08B0	21FE3D	LXI	H,MSG6	:DISPLAY JULIAN DATE
08B3	CC7F08	CALL	MSG	:
08B6	21A43C	LXI	H,DATE	:POINT TO JULIAN DATE ADDR
08B9	CC6608	CALL	CONIN	:INPUT CHAR FM CONSOLE
08BC	CC7208	CALL	CONOUT	:OUTPUT CHAR TO CONSOLE
08BF	FE20	CPI	:	:IF SPACE BAR JMP A/C TYPE
08C1	CAC508	JZ	REC2	:
08C4	77	MOV	M,A	:STORE JULIAN DATE
08C5	23	INX	H	:INCREMENT ADDRESS
08C6	C3B908	JMP	JDATE	:GET NEXT DIGIT
08C9	21243E	REC2:	LXI H,MSG7	:DISPLAY AIRCRAFT TYPE
08CC	CC7F08	CALL	MSG	:
08CF	21BA3C	LXI	H,TYP	:POINT TO A/C TYPE ADDR
08D2	CC6608	ACTYPE:	CALL CONIN	:INPUT CHAR FM CONSOLE
08D5	CC7208	CALL	CONOUT	:OUTPUT CHAR TO CONSOLE
08D8	FE20	CPI	:	:IF SPACE BAR JMP SICENG
08DA	CAE208	JZ	REC3	:
08DD	77	MOV	M,A	:STORE TYPE ADDRESS
08DE	23	INX	H	:INCREMENT NEXT CHAR
08DF	C3D20E	JMP	ACTYPE	:GET NEXT CHAR
08E2	214E3E	REC3:	LXI H,MSG8	:DISPLAY BUNC
08E5	CC7F08	CALL	MSG	:
08E8	21D03C	LXI	H,SIDNO	:POINT TO SIDE NUMBER ADDR
08EE	CC6608	SDNUM:	CALL CCNIN	:INPUT CHAR FM CONSOLE
08F1	CC7208	CALL	CONOUT	:OUTPUT CHAR TO CONSOLE
08F3	FE20	CPI	:	:IF SPACE BAR JMP CONFIGURATION
08F6	CAFB08	JZ	REC4	:
08F7	77	MOV	M,A	:STORE SIDE NUMBER
08F8	23	INX	H	:INCREMENT ADDR
08F8	C3EB0E	JMP	SDNUM	:GET NEXT CHAR
08FB	216F3E	REC4:	LXI H,MSG9	:DISPLAY CONFIGURATION
08FE	CC7F08	CALL	MSG	:
0901	21E83C	LXI	H,CNFIG	:POINT TO CONFIG ADDR
0904	CC6608	CNFIG:	CALL CCNIN	:INPUT CHAR FM CONSOLE
0907	CC7208	CALL	CONOUT	:OUTPUT CHAR TO CONSOLE
090A	FE20	CPI	:	:IF SPACE BAR JMP GRCS WEIGHT
090C	CA1409	JZ	REC5	:

0920	MOV	M,A	:STORE CONFIG.
0921	INX	H	:INCREMENT ADDR
0922	JMP	CNFIG	:GET NEXT CHAR
0923	REC5:	LXI	H,MSGD ;DISPLAY GROSS WEIGHT
0924	CALL	MSG	
0925	LXI	H,GSWT	:POINT TO GROSS WEIGHT ADDR
0926	CALL	CONIN	:INPUT CHAR FM CCNSOLE
0927	CONOUT	:	:OUTPUT CHAR TO CONSOLE
0928	CPI	:	:IF SPACE BAR JMP MISSION
0929	JZ	REC6	
0930	MOV	M,A	:STORE GROSS WEIGHT
0931	INX	H	:INCREMENT ADDR
0932	JMP	GWT	:GET NEXT CHAR
0933	REC6:	LXI	H,MSGB ;DISPLAY MISSION
0934	CALL	MSG	
0935	LXI	H,MSN	:POINT TO MISSION ADDR
0936	CALL	CONIN	:INPUT CHAR FM CCNSOLE
0937	CONOUT	:	:OUTPUT CHAR TO CONSOLE
0938	CPI	:	:RETURN IF SPACE BAR
0939	RZ	:	
0940	MOV	M,A	:STORE MISSION
0941	INX	H	:INCREMENT ADDR
0942	JMP	MSSN	:GET NEXT CHAR
0943	REC7:	LXI	H,MSGD ;DISPLAY SCALE FACTOR
0944	CALL	MSG	
0945	LXI	H,SCALE	:POINT TO SCALE ADDRESS
0946	CALL	CONIN	:INPUT CHAR FM CCNSOLE
0947	CONOUT	:	:OUTPUT CHAR TO CONSOLE
0948	CPI	:	:IF SPACE BAR JMP LIMIT
0949	JZ	REC8	
0950	MOV	M,A	:STORE SCALE FACTOR
0951	INX	H	:INCREMENT ADDR
0952	JMP	SCALE	:GET NEXT CHAR
0953	REC8:	LXI	H,MSGDI ;DISPLAY LIMIT LOAD
0954	CALL	MSG	
0955	LXI	H,LIMIT	:POINT TO LIMIT ADDR
0956	CALL	CONIN	:INPUT CHAR FM CCNSOLE
0957	CONOUT	:	:OUTPUT CHAR TO CONSOLE
0958	CPI	:	:IF SPACE BAR JMP AREA
0959	JZ	REC9	
0960	MOV	M,A	:STORE LIMIT LOAD
0961	INX	H	:INCREMENT ADDR
0962	JMP	LMTLD	:GET NEXT CHAR

09EA 3E00	MVI A,0H ;WRITE THREE ZEROS
09EC CDC709	CALL WRCHAR
09EF CCC709	CALL WRCHAR
09F2 CCC709	CALL WRCHAR
09F5 7A	MOV A,D ;WRITE FILE TYPE
09F6 CCC709	CALL WRCHAR
09F9 79	MOV A,C ;WRITE RECORD LENGTH
09FA CDC709	CALL WRCHAR
09FC 7E	NEXTCH: MOV A,M ;ADDR OF FIRST CHAR
09FE CCC709	CALL WRCHAR ;OUTPUT CHAR CN TAPE
0AC1 80	ADD E ;UPDATE CHECK SUM
0A02 41	MOV B,A
0A03 22	INX H ;MOVE POINTER
0AC4 CD	DCR C ;DECREASE CCOUNTER
0A05 C2FD09	JNZ NEXTCH ;JMP NEXT CHAR
0A08 7E	MOV A,M ;PUT CHAR IN ACCM
0A09 CDC709	CALL WRCHAR ;OUTPUT CHAR ON TAPE
0A0C 80	ADD B ;UP DATE CHECKSUM
0A0D 47	MOV B,A ;
0A0E 7E	MOV A,B ;MOVE CHKSUM INTO ACCM
0A0F CCC709	CALL WRCHAR ;OUTPUT CHKSUM CN TAPE
0A12 CC8A08	CALL GAP ;BLANK TAPE
0A15 C5	RET
0A16 2101F7
0A19 3A473C
0A1C 4F
0A1D 3A483C
0A20 B1
0A21 77

SUBROUTINE 'RANCHN' USES THE GLOBAL VARIABLES 'FSTCHN' AND
 'GAIN' TO DEFINE THE CHANNEL GAIN TO BE USED IN THE CCNVERSION
 OF THE ANALOG INPUT. THE RESULT IS STORED AT THE LOCATION
 POINTED BY 'DATPTR'.

RANCHN: LXI H,FCR ;POINT HL TO FIRST CHANNEL REGISTER.
 LDA GAIN ;LOAD GAIN
 MOV C,A
 LDA FSTCHN ;LOAD CHANNEL.
 ORA C ;ADD GAIN BITS
 MOV M,A ;LOAD FIRST CHANNEL REG.

```

0A22 2B      DCX H      ;POINT TO COMMAND/STATUS REG.
0A23 3E01    MVI M,GO   ;START CCAVERSION

0A25 7E      MOV A,M     ;READ STATUS
0A26 01      RLC         ;CHECK EOC STATUS
0A27 D2250A  JNC RAN1    ;JMP IF CCAVERSION NCT DONE

0A2A 3E00    MVI M,0     ;RESET CCAV-ENABLE IN CMC REG.
0A2C CD300A  CALL LODATA ;LCAD DATA INTO MEMORY

0A2F C5      RET        ;EXIT RANCM

;
;
;SUBROUTINE 'LODATA' TAKES THE A/C CCAV DATA AND
;LOADS IT INTO THE MEMORY LOCATION POINTED BY 'DATPTR'.
;
;
LODATA:
0A30 2A04F7  LHLD ADDATA ;LOAD CONVERTED DATA INTO HL
0A33 EE      XCHG       ;PUT CONVERTED DATA INTO DE

0A34 F5      PUSH PSW   ;SAVE ACCM AND FLAGS
0A35 2A01F7  LDA FCR     ;LOAD GAIN, MUX ADDR REG
0A36 E6CF    ANI OFH    ;SAVE CHANNEL NUMBER TO DATA
0A37 B3      ORA E       ;ADD CHANNEL NUMBER TO DATA
0A38 5F      MOV E,A     ;STORE LSEYTE + CHAN NUMB IN REG E.
0A3C F1      POP PSW    ;RESTORE ACCM AND FLAGS

0A3C 2A4B3C  LHLD DATPTR ;LOAD ADDR OF DATA POINTER INTO HL
0A40 72      MOV M,D     ;STORE DATA INTO MEMCRY
0A41 2C      INR L       ;INCREMENT DATA POINTER
0A42 7C      MOV A,L     ;MOV BUFFER COUNTER INTO ACCM
0A43 324A3C  STA KOUNTR  ;STORE BUFFER CCOUNTER IN KCUNTR

0A46 73      MOV M,E     ;STORE LSBYTE DATA INTO MEMORY

0A47 FEFF    CPI OFFH   ;BUFFER FULL
0A48 CC7F0A  CZ DUMP    ;CALL DUMP IF BUFFER FULL

0A4C 2C      INR L       ;INCREMENT DATA POINTER
0A4D 7D      MOV A,L     ;MOV BUFFER COUNTER INTO ACCM
0A4E 324A3C  STA KOUNTR ;STORE BUFFER CCOUNTER IN KCUNTR

0A51 224B3C  SHLD DATPTR ;SAVE NEW ADDR CF DATA PCINTER

```



```

FUNCTION: STCP
INPUT : NONE
OUTPUT : A-CHAR VIA PORTC
CALLS : NONE
DESTROYS: A
DESCRIPTION: REMOVES START SIGNAL FROM CASSETTE DRIVE

```

```

STOP:
MVI A,0B0H ;REMOVE START SIGNAL
OUT PORTC ;OUTPUT TO PORTC
RET ;EXIT STCF

```

```

FUNCTION: DUMP
INPUT : NONE
OUTPUT : D-RECORD TYPE
          C-RECORD LENGTH
          H,L-START ADDR OF RECORD
CALLS : WRREC
DESTROYS:
DESCRIPTION: WRITE A RECORD OF DATA ON TAPE-
            RECORD LENGTH DETERMINED BY KCUNTR.

```

```

DUMP:
PUSH F ;SAVE ADDRESS OF DATA POINTER
LXI H,KCUNTR ;POINT TO ADDR CF COUNTER
MOV C,M ;RECORD LENGTH
LXI H,BUFR ;PCINT TO INITIAL ADDR OF MEMORY
MVI D,03H ;RECORD TYPE
CALL WRREC ;WRITE RECORD ON TAPE
CALL STOP ;STOP CASSETTE TAPE
POP F ;RESTORE HL
RET ;EXIT DUMP

```

```

0A7A 3EB0
0A7C C3EA
0A7E C5

```

```

0A7F E5
0A80 214A3C
0A83 4E
0A84 210043
0A87 1603
0A89 CDE509
0A8C CD7A0A
0A8F E1
0A90 C5

```


0AE3 0600	MVI B,0H ; INITIALIZE CHKSUM	RDEC:	CALL RDCHAR
0AE5 CDC00A	OH ; READ FIRST BYTE	RDC1:	OH ;
0AE8 FE00	RDC1 ; CONTINUE FIRST NON ZERC BYTE	JNZ	
0AEA C2E50A			
0AEC CCC00A	CALL RDCHAR ; GET FILE TYPE	RDC2:	CALL RDCHAR ; GET FILE TYPE
0AF0 FE00	OH ;	JZ	
0AF2 CAED0A	RDC2 ; CONTINUE NOT ZERO		
0AF5 57	MOV D,A ; STORE FILE TYPE		
0AF6 CDC00A	CALL RDCHAR ; GET FILE LENGTH		
0AF8 4F	MOV C,A ; STORE FILE LENGTH		
0AFA F5	PUSH ; SAVE FILE LENGTH ON STACK		
0AFB CCC00A	CALL RDCHAR ; GET NEXT DATA BYTE	RDC3:	CALL RDCHAR ; GET NEXT DATA BYTE
0AFF 77	M,A ; STORE DATA IN RAM	MOV	
0AFF 80	ADD ; UPDATE CHECKSUM	ADD	
0B00 47	MOV B,A ; STORE NEW CHECKSUM	MOV	
0B01 2E	INX H ; INCREMENT DATA BUFFER	DCR	
0B02 0C	INX C ; INCREMENT BUFFER COUNTER	JNZ	
0B03 C2FB0A	RDC3 ; COUNTER NOT ZERO GET NEXT DATA BYTE	CALL	
0B06 CDC00A	RDCHAR ; GET LAST BYTE	CALL	
0B09 77	M,A ; UPDATE CHECKSUM	ADD	
0B0A 80	ADD ;	MOV	
0B0B 47	MOV B,A ;	POP	
0B0C F1	PSW ; GET FILE LENGTH FROM STACK	MOV	
0B0C 4F	C,A ; FILE LENGTH INTO C		
0BCE CDC00A	CALL RDCHAR ; GET CHECKSUM FROM TAPE		
0B11 A8	XRA B ; IF ERROR ACCM NOT ZERC		
0B12 E5	PUSH H ; SAVE RAM ADDRESS		
0B13 21B03D	LXI H,MSG4 ; ADDR CHECKSUM ERROR MSG		
0B16 C47F08	CNZ MSG ; ACCM NOT ZERO PRINT MSG2		
0B19 E1	POP H ; RESTOR RAM ADDRESS		
0B1A C5	RET ; EXIT RDEC		

FUNCTION: DSFILE
INPUT: B-CHECKSUM
C-FILE LENGTH
D-FILE TYPE
OUTPUT: C-CHARACTER CA CONSOLE
CALLS: CONOUT, NMOUT

0818 15	DSFILE:	C		
081C CA2B0B	DCR	DSFIL1		;JMP FILE TYPE CNE
	JZ			
081F 15	DCR	D		
0820 CA2B0B	JZ	DSFIL2		;JMP FILE TYPE TWO
0823 15	DCR	D		
0824 CA430B	JZ	DSFIL3		;JMP FILE TYPE THREE
0827 15	DCR	D		
0828 CA930B	JZ	DSFIL4		;JMP FILE TYPE FOUR
082B 21C043	DSFIL1:			
082E 22483C	DSFIL2:	H,BUFR		;POINT START OF MEMORY BUFFER
	LXI	DATPTR		;STORE ADDRESSES OF BUFR
	SHLD			
0831 2A4B3C	DSF1A:	LHLD DATPTR		;POINT ADDRESS OF BUFR
0834 7E	MOV	A,M		;PUT DATA IN ACCM AT ADDRESS IN FL
0835 CE	PUSH	B		;SAVE CHECKSUM AND FILE LENGTH
0836 CD7208	CALL	CONOUT		;OUTPUT CHAR TO CONSOLE
0839 C1	POP	B		;RESTORE CHECKSUM AND FILE LENGTH
083A 2C	INR	L		;INCREMENT MEMORY BUFFER POINTER
083B 22483C	SHLD	DATPTR		;SAVE NEW POINTER
083E 0D	DCR	C		;DECREMENT MEMORY COUNTER
083F C2310B	JNZ	DSF1A		;JMP COUNTER NOT ZERO
0842 C5	RET			;RETURN
0843 21C043	DSFIL3:	H,BUFR		;POINT TO START MEMORY BUFFER
0846 22483C	LXI	DATPTR		;STORE ADDRESS CF BUFR IN HL
	SHLD			
0849 21E03D	LXI	H,MSG5A		;PROMPT-HARD CCFY STRAIN DATA
084C CD7F08	CALL	MSG		;DISPLAY MESSAGE
084F CD6608	CALL	CCININ		;INPUT CHAR FROM CONSOLE
0852 CD7208	CALL	CONOUT		;OUTPUT CHAR TO CCNSCLE
0855 FE59	CPI	Y		;IF YES DATA DISPLAYED
0857 C28C0B	JNZ	DSF3C		;IF NCT ZERO SKIP DISPLAY
085A CC9C0B	CALL	CROUT		;OUTPUT CR LF
	DSF3A:			
085D 3E20	MVI	A,' '		;MOV ASCII SPACE CHAR INTO ACCM
085F CC7208	CALL	CONCUT		;OUTPUT SPACE TO CONSOLE
0862 2A4B3C	DSF3B:	LHLD DATPTR		;POINT TC BUFFER ADDRESS

0B65 7E	MOV A,M ;PUT DATA IN ACCM
0B66 C5	PUSH B ;SAVE CHECKSUM AND FILE LENGTH
0B67 F5	PUSH PSW ;SAVE ACCM - FLAGS
0B68 CC202	CALL NMOUT ;CONVERT 8-BIT BINARY TC TWC ASCII CHAR
0B6B F1	POP PSW ;RESTORE ACCM - FLAGS
0B6C C1	POP B ;RESTORE CHECKSUM - FILE LENGTH
0B6C 22	INX H ;INCREMENT MEMORY BUFFER POINTER
0B6E 224B3C	SHLD DATPTR ;SAVE NEW DATA BUFFER POINTER
0B71 0D	DCR C ;DECREMENT MEMORY BUFFER COUNTER
0B72 C48C0B	JZ DSF3C ;IF ZERO OUTPUT CR LF
0B75 7C	MOV A,L ;LOAD LSBYTE OF HL
0B76 E60F	ANI 0FH ;CHECK FOR END OF LINE
0B7E C2810B	JNZ DSF3D ;NC-CHECK FOR SECCND BYTE
0B7E C19C0B	CALL CROUT ;YES-OUTPUT CR LF
0B7E C35D0B	JMP DSF3A ;JMP COUTPUT SPACE
0B81 7C	DSF3D: MOV A,L ;LOAD LSBYTE OF HL
0B82 E601	ANI 0FH ;CHECK FOR SECOND BYTE
0B84 FE01	CPI 01H ;
0B86 CA620B	JZ DSF3B ;NO-GET NEXT BYTE
0B89 C35D0B	JMP DSF3A ;YES-OUTPUT SPACE
0B8C C19C0B	DSF3C: CALL CROUT ;OUTPUT CR LF
0B8F C19C0B	CALL CROUT ;
0B92 C9	RET ;
0B93 21D03D	DSFIL4: LXI H,MSG5 ;LOAD MSG3 ADDRESS
0B96 C07F0B	CALL MSG ;
0B99 C3860F	JMP MAIN ;
0B9C 3E0D	CROUT: A,CR ;OUTPUT CR
0B9E C0720B	MVI CONOUT ;OUTPUT TO CONSOLE
0BA1 3E0A	MVI A,LF ;OUTPUT LF
0BA3 C0720B	CALL CONOUT ;OUTPUT TO CONSOLE
0BA6 C5	RET ;EXIT CROUT
	GOUGE:

0C22 2AC4F7	VALDT: LHL D ADDATA ;LOAD CONVERTOR DATA INTO HL
0C23 EB	XCHG ;CONVERTOR DATA INTO DE
0C26 2A9642	LHL D UPPER ;CHECK UPPER BOUNDARY
0C25 CDA002	CALL HILO ;CARRY BIT (0-H DE)
0C2C DA4D0C	JC VALDT2 ;CHECK LOWER BOUNDARY
0C2F 3EFF	MVI A,OFFH ;SET FLAG
0C31 32543C	STA FLAG ;
0C34 2A513C	LHL D XLST ;LOAD LAST X INTO HL
0C37 CCA002	CALL HILO ;CARRY BIT (0-HDE)
0C3A C2AE0C	JNC VALDT7 ;JMP UPDATE LASTX
0C3D 7A	MOV A,D ;
0C3E 54	SUB F ;
0C3F CABD0C	JZ VALDT9 ;
0C42 3A533C	LDA SIGN ;LOAD SIGN
0C45 FEC1	CPI 01H ;
0C47 CAAE0C	JZ VALDT7 ;JMP UPDATE UPDATE LASTX
0C4A C35E0C	JMP VALCT5 ;JMP IF LASTX >= X
0C4D 2A5842	VALDT2: LHL D LOWER ;CHECK LOWER BOUNCARY
0C5C CDA002	CALL HILO ;CARRY BIT (0-HL DE)
0C53 D26F0C	JNC VALDT4 ;JMP IF X LOWER
0C56 3EFF	MVI A,OFFH ;SET FLAG OFFH
0C58 32543C	STA FLAG ;
0C5B 2A513C	LHL D XLST ;LOAD LASTX
0C5E CCA002	CALL HILO ;CARRY BIT (0- HDE)
0C61 CAAE0C	JC VALDT7 ;JMP UPDATE LASTX
0C64 3A533C	LDA SIGN ;LOAD SIGN FLAG

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DESIGN OF SOFTWARE PACKAGE FOR INCORPORATION OF RANDOM LOAD TEST--ETC(U)

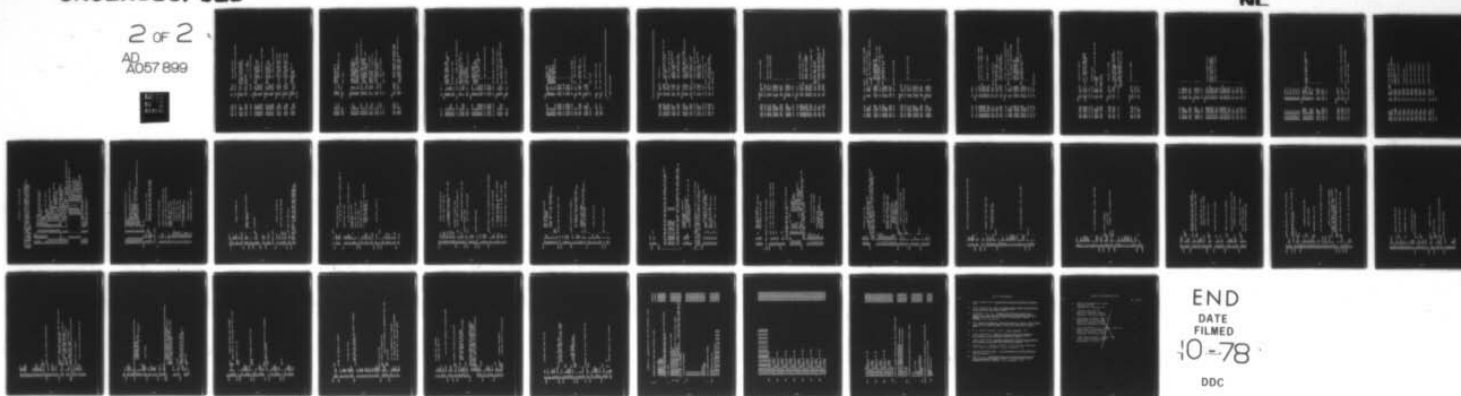
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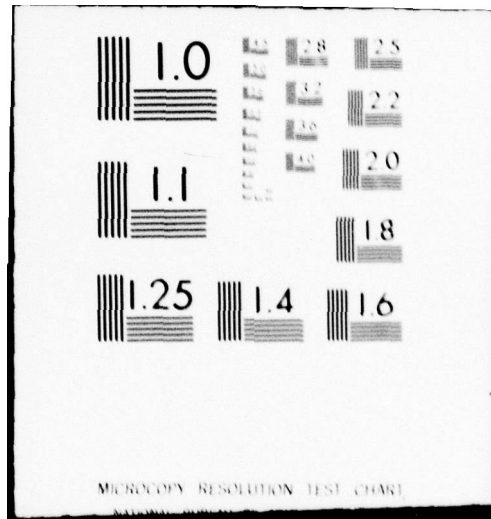
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CC67 FE00	CPI	00H	;	JMP	UPDATE LASTX
OC65 CAAE0C	JZ	VALDT7			
OC6C C3A60C	JMP	VALDT6		;	CHANGE SIGN-UPDATE LASTX-STCRE
OC6F 3A543C	VALDT4:				
OC72 FEFF	LDA	FLAG	;	POINT TO FLAG BUFFER	
OC74 CC	CPI	OFFH	;	IF FLAG SET CONTINUE	
	RNZ		;	RETURN IF FLAG NOT SET	
OC75 2A513C	LHLD	XLST	;	HL LASTX	
OC7E CCA002	CALL	HILO	;	CARRY BIT (0- HDE)	
			;	(1- HL>DE)	
OC7B DA8E0C	JC	NEGSN		;	CHECK FCR SIGN CHANGE
OC7E 3A533C	PCSSGN:				
OC81 FE00	LDA	SIGN	;	PUT SIGN IN ACCM.	
OC83 CAAE0C	CPI	00H	;	COMPARE NEGATIVE SIGN	
OC86 3E00	JZ	VALDT7	;	JMP UPDATE LASTX	
OC88 32543C	MVI	A,00H	;	SET FLAG 00H	
OC8B C3A60C	STA	FLAG	;		
	JMP	VALDT6	;	CFG SIGN-UPDATE LASTX-STORE	
OC8E 3A533C	NEGSN:				
OC91 FE01	LDA	SIGN	;	PUT SIGN IN ACCM.	
OC93 CAAE0C	CPI	01H	;	COMPARE NEGATIVE SIGN	
OC96 3E00	JZ	VALDT7	;	JMP UPDATE LASTX	
OC98 32543C	MVI	A,00H	;	SET FLAG 00H	
OC9B C39E0C	STA	FLAG	;		
	JMP	VALDT5	;	CHG SIGN-UPDATE LASTX-STCRE	
OC9E 3E01	VALDT5:				
OCAC 32533C	MVI	A,01H	;	CHANGE SIGN (NEGATIVE)	
OCA3 C3B50C	STA	SIGN	;	STORE IN SIGN BUFFER	
	JMP	VALDT8	;	JMP UPDATE LASTX-STORE	
OCA6 3E00	VALDT6:				
OCA8 32533C	MVI	A,00H	;	CHANGE SIGN (PCSSITIVE)	
OCA8 C3E50C	STA	SIGN	;	STORE IN SIGN BUFFER	
	JMP	VALDT8	;	JMP UPDATE LASTX-STORE	
OCAE EB	VALDT7:				
OCAF 22513C	XCHG	XLST	;	EXCHANGE DE AND HL	
CCB2 C3BD0C	SHLD	VALDT9	;	UPDATE LASTX	
	JMP		;	RETURN	
	VALDT8:				

OCB5 EB	XCHG	XLST	; EXCHANGE DE AND HL
OCB6 22513C	SHLD		; UPDATE LASTX
OCB5 EB	XCHG		; EXCHANGE CE AND HL
OCBA CDEFOC	CALL	STORE	; STORE SIGNIFICANT DATA
CCBD OC	VALDT9:	NOP	
OCBE CS	RET		; EXIT VALDT
STORE:			
CCBF F5	PUSH	PSW	; SAVE ACCM AND FLAGS
OCCE 3A01F7	LOA	FCR	; LOAD GAIN, MUX ADDR REG
OCCE E60F	ANI	OFH	; SAVE CHANNEL NUMBER
OCCE 83	ORA	E	; ADD CHN NUMBER TO DATA
OCCE 5F	MOV	E, A	; STORE LSEYTE + CHAN NUMBER IN REG E
OCCE 7F1	POP	PSW	; RESTORE ACCM AND FLAGS
OCCE 2A4B3C	LHLD	DATPTR	; LOAD ADDR. OF DATA POINTER INTO HL
OCCE 72	MOV	M, D	; STORE DATA INTO MEMORY
OCCE 2C	INR	L	; INCREMENT DATA POINTER
OCCE 7C	MOV	A, L	; MOV BUFFER COUNTER INTO ACCM
OCCE 324A3C	STA	KOUNTR	; STORE COUNTER
OCCE 72	MOV	M, E	; STORE LSEYTE INTO MEMORY
OCCE 7EFF	CPI	OFFH	; BUFFER FULL
OCCE CC7FOA	CZ	DUMP	; DUMP MEMORY IF BUFFER FULL
OCCE 2C	INR	L	; INCREMENT DATA POINTER
OCCE 7C	MOV	A, L	; MOVE BUFFER COUNTER INTO ACCM
OCCE 324A3C	STA	KOUNTR	; STORE COUNTER
OCCE 224B3C	SHLD	DATPTR	; SAVE DATA POINTER
OCCE CS	RET		; EXIT STORE
RAMP:			
OCCE 210044	LXI	H, BUFRI	; POINT RANDOM LCAD BUFFER ADDR
OCCE 224C3C	SHLD	BFPTRI	; STORE RANDOM LCAD BUFFER ADDR
OCCE 2A4D3C	LHLD	BFPTRI	; LCAD FANDOM LOAC BUFFER ADDR INTO HL
OCCE 56	MOV	D, M	; LOAC RANCCM DATA INTO DE
OCCEA 23	INX	H	; INCREMENT BUFFER ADDR

0CEB 5E	MOV E,M ;	
0CEC 23	RMP1: INX H ; INCREMENT BUF1 ADDR	
0CED 46	MOV B,M ; LOAD NEXT RANDOM DATA INTO BC	
0CEE 7E	MOV A,M ; CHECK FPCR 1AH E O D SEED	
0CEF FE1A	CPI 1AH ;	
0CF1 CA590D	RMP9: JZ RMP9 ; INCREMENT BUF1 ADDR	
0CF4 2E	INX F ;	
0CF5 4E	MOV C,M ;	
0CF6 224D3C	RMP3: SHLD BFPTRI ; STORE RANCCM LOAD BUFFER ACCR	
0CF9 7E	MOV A,B ; MOV MSBYTE BC INTO ACCM.	
0CFA BA	CMP D ; COMPARE MSBYTE OF BC WITH DE	
0CFB CA510D	JZ RMP2 ; D=B JMP RESTART 1	
0CFE DA2C0D	RMP8: JC RMP8 ; D > B JMP	
0D01 6E	MOV H,D ; D B CONTINUE	
0D02 6E	MOV L,C ; LOAD DE INTO HL	
	; SET LS BYTE IN BC EQUAL IN HL	
0D03 C610D	RMP4: CALL WDACO ; OUTPUT DACO	
0DC6 24	INR F ; INCREMENT MSBYTE HL	
0D07 7C	MOV A,H ; LOAD MSBYTE INTO ACCM	
0D08 B8	CMP B ; COMPARE WITH MSBYTE BC	
0D09 CA240D	JZ RMP5 ; H=B ; GET NEXT DATA BYTE	
0D0C CCE104	CALL DELAY ; EACH DELAY = 1PSEC	
0D0F CCE104	CALL DELAY ;	
0D12 CCE104	CALL DELAY ;	
0D15 CD000C	CALL SGLCHN ; CHECK SELECTED CHAN FOR DATA	
0D18 CCE104	CALL DELAY ;	
0D1B CDB104	CALL DELAY ;	
0D1E CDB104	CALL DELAY ;	
0D21 C3030D	JMP RMP4 ; JMP CONTINUE TO INCFEMENT MSEYTE	
0D24 54	RMP5: MOV D,H ; UPDATE DE MAINTAIN IACO CUTPUT	
0D25 5D	MOV E,L ;	
0D26 2A4D3C	LHLD BFPTRI ; LOAD HL WITH DATA BUFFER ADDRESS	
0D29 C3EC0C	JMP RMP1 ; JMP GET NEXT DATA BYTE	
0D2C 6E	RMP8: MOV H,D ; MOV DE INTO HL	
0D2D 6E	MOV L,C ;	

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002E C610D      RMP7: CALL WDACO ;OUTPUT DACO
0031 FEF       PUSH PSW ;SAVE FLAG;
0032 2F       DCR H ;DECREMENT MSBYTE
0033 2F       POP PSW ;RESTORE FLAG;
0034 7C       MOV A,H ;MSBYTE INTO ACCM
0035 BE       CMP B ;COMPARE MSBYTE BC
0036 CA240D    JZ RMP5 ;JMP GET NEXT DATA BYTE

0C39 CCB104    CALL DELAY ;
0C3C CCB104    CALL DELAY ;
0C3F CCB104    CALL DELAY ;

0D42 CDC00C    CALL SGLCHN ;

0D45 CDB104    CALL DELAY ;
0D46 CCB104    CALL DELAY ;
0D48 CDB104    CALL DELAY ;

0D4E C32E0D    JMP RMP7 ;CONTINUE TO DECREMENT

0D51 62       RMP2: MOV H,D ;MOVE DE REG INTO HL
0D52 65       MOV L,C ;

0D53 C610D     CALL WDACO ;OUTPUT DACO
0D56 C3240D    JMP RMP5 ;JMP GET NEXT LOAD DATA

0D59 2680     RMP9: MVI H,80H ;SET DACO ZERO VCLTS
0D5B 2E00     MVI L,00H ;
0D5C C610D     CALL WDACO ;
0D60 C9       RET ;EXIT RAMP

0D61 2206F7    WDACO: SHLC DACO ;WRITE TO DACO
0D64 C5       RET

0D65 22CAF7    WDACL: SHLC DAC1 ;WRITE TO DAC1
0D68 C5       RET

```

```

*****
;
;
; EXECUTIVE ROUTINES
*****

```

ADDRESS	INSTR	OPERAND	COMMENT
0069	21AB40		
006C	CC7F08		
006F	CC5608		
0072	3A9A42		
0075	FE41		
0077	C2EA0D		
007A	CDA609		
007D	21803C		
0080	1601		
0082	0E95		
0084	CCE509		
00E7	C3970D		
008A	CDA609		
008C	21153D		
0090	1602		
0092	0E9A		
0094	CCE509		
0097	CC7A0A		
009A	CC550A		
009C	21803D		
00AC	1604		
00A2	0E0D		
00A4	CCE509		
00A7	CC7A0A		
00AA	21FA3E		
00AD	CC7F08		
00B0	C3860F		

0DB3 21B240	ACCHN:	H,MSGG2	:DISPLAY A> PROMPT
0DB6 CC7F08	LXI	MSG	:
	CALL	MSG	:
0DB5 CC5608	CALL	HEADING	:
0DBC 3F9A42	LDA	TPDPTA	:PUT DATA TYPE IN ACCM
0DBF FE41	CPI	A	:
0DC1 C2D40D	JNZ	ADC01	:ACTUAL=A SIMULATED=S
0DC4 CCA609	CALL	OXIDE	:
0DC7 21B03C	LXI	H,MSG1	:
0DCA 1601	MVI	D,01H	:
0DCC CE55	MVI	C,95H	:
0DCE CDE509	CALL	WRREC	:
0DC1 C3E10D	JMP	ADC02	:
0DC4 CCA609	CALL	OXIDE	:
0DD7 21153D	LXI	H,MSG2	:
0DCA 1602	MVI	D,02H	:
0DCC CE5A	MVI	C,9AH	:
0DCE CCE509	CALL	WRREC	:
0DE1 CC7A0A	ADC02:	CALL	STOP
0DE4 217E40	LXI	F,MSGF	:DISPLAY SELECT CHANNEL #
0DE7 CC7F08	CALL	MSG	:
0DEA 21483C	LXI	H,FSTCHN	:POINT TO FIRST CD
0DEL CC6608	CALL	CONIN	:INPUT CHAR FROM CONSOLE
0DFC CC7208	CALL	CONOUT	:ECHO CHAR TO CONSOLE
0DF3 4F	MOV	C,A	:
0DF4 CDDF01	CALL	CNV8N	:
0DF7 32483C	STA	FSTCHN	:STORE IN FIRST CHANNEL REG
0DFA 3ECC	MVI	A,X1	:SET GAIN EQUAL TO ONE
0DFC 32473C	STA	GAIN	:STORE IN GAIN BUFFER
0DFF 3ECC	MVI	A,0H	:INITIALIZE BUFFER COUNTER TC ZERO
0E01 324A3C	STA	KOUNTR	:STORE IN COUNTER BUFFER
0EC4 21C043	LXI	H,BUFR	:POINT TC DATA BUFFER
0E07 22483C	SHLD	DATPTR	:SORCLC POINTER FOR RANCH

0ECA CD160A	ADC03: CALL RANCHN ;CONVERT SELECTED CHANNEL
CECD DBED	IN CRTS ;DEPRESS ANY KEY TO END MISSION
OE0F E6C2	RBR ;
OE11 CA0A0E	ADC03 ;IF ZERO CONTINUE TO CONVERT SELECTED CHANNEL
OE14 CC6608	CALL CONIN ;REAL DEPRESS KEY
OE17 CC7208	CALL CONOUT ;ECHO DEPRESS KEY
OE1A FE04	CPI CNTRLD ;IF CNTRLD DUMP BUFFER ON TAPE
OE1C C20A0E	JNZ ADCC3 ;TERMINATE MISSION
OE1F CD7F0A	CALL DUMP ;DUMP CCNVEFTED DATA ON TAPE
OE22 21803D	LXI H,MSG3 ;LOAD END OF FILE
OE25 1604	MVI D,04H ;
OE27 0E0D	MVI C,0DH ;
OE29 CCE509	CALL WRREC ;
OE2C CC7A0A	CALL STOP ;
OE2F C3860F	JMP MAIN ;RETURN TO MAIN ROUTINE
0E32 218940	ADCMXM: LXI F,MSGG3 ;
0E33 CC7F08	CALL MSG ;
0E3E CC5608	CALL HEADNG ;
0E3B 3A9A42	LDA TYPDTA ;PUT TYPE DATA IN ACCM
0E3E FE41	CPI 'A' ;
0E40 C2530E	JNZ ACCR1 ;
0E43 CCA609	CALL OXIDE ;
0E46 21803C	LXI H,MSG1 ;
0E49 1601	MVI D,01H ;
0E4B 0E95	MVI C,95H ;
0E4D CDE509	CALL WRREC ;
0E50 C3600E	JMP ADCR2 ;
0E53 CCA609	ADCR1: CALL CXIDE ;
0E56 21153D	LXI H,MSG2 ;
0E59 1602	MVI D,02H ;
0E5B CE5A	MVI C,9AH ;

0E5C CCE509	CALL WRREC ;	
0E6C CC7A0A	ADCR2: CALL STOP ;	
0E63 217E40	LXI H,MSGF ;	DISPLAY SELECT CHANNEL NUMBER
0E66 CC7F08	CALL MSG ;	
0E69 21483C	LXI H,FSTCHN ;	PCINT TO FIRST CHANNEL EUFFER
0E6C CC66C8	CALL CONIN ;	
0E6F CD7208	CALL CCNCUT ;	
0E72 4F	MOV C,A ;	
0E73 CDDF01	CALL CNVBN ;	CONVERT ASII TC BINARY
0E76 32483C	STA FSTCHN ;	
0E79 3EC0	MVI A,XI ;	
0E7B 32473C	STA GAIN ;	STORE IN GAIN BUFFER
0E7E 3E00	MVI A,OH ;	BUFFER COUNTER ZERO
0E8C 324A3C	STA KOUNTR ;	STORE IN KOUNTF EUFFER
0E83 210043	LXI F,BUFR ;	PCINT TO DATA EUFFER
0E8E 22483C	SHLD DATPTR ;	LOAD POINTER FOR RANCHN
0E89 CDC00C	ADCR3: CALL SGLCHN ;	CCNVERT SINGLE CHN (PEAKS/VALLEYS)
0E8C DBED	IN CRTS ;	DEPRESS CONTROL 0 TO END MISSION
0E8E E602	ANI RBR ;	
0E90 C890E	JZ ADCR3 ;	IF ZERO CONVERT SELECTED CHANNEL
0E93 CC6608	CALL CONIN ;	READ DEPRESSED KEY
0E96 CD7208	CALL CONOUT ;	ECHO DEPRESSED KEY
0E99 FE04	CPI CNTRL0 ;	IF CNTRL0 DUMP BUFFER
0E9B C2890E	JNZ ADCR3 ;	NOT CONTROL C CONVERT SELECTED CHANNEL
0E9E CD7F0A	CALL DUMP ;	DUMP CACNVERTED DATA CN TAPE
0EA1 21803D	LXI H,MSG3 ;	LOAD END OF FILE
0EA4 1604	MVI D,04H ;	
0EA6 0E0D	MVI C,0CH ;	
0EA8 CCE509	CALL WRREC ;	
0EAB CD7A0A	CALL STOP ;	
0EAE C3860F	JMP MAIN ;	RETURN MAIN

0EB1 21C040	READ:	H,MSGG4	:DISPLAY R> PROMPT
0EB4 C07F08	LXI	MSG	:
	CALL	MSG	:
0EB7 CDA50A	CALL	CLROX	:PUT READ HEAD NEAR OXIDE
0EBA 210043	READ1:	LXI H, BUFR	:POINT TC DATA BUFFER
0EBC CCE30A	CALL	ROREC	:REAC FILE INTO MEMORY
0ECC C07A0A	CALL	STOP	:
0EC3 C01B0B	CALL	DSFILE	:DISPLAY FILE
0EC6 DBEA	IN	PORTC	:CHECK PORTC STATUS FOR END OF TAPE
0EC8 E608	ANI	08H	:00001000
0ECA C2BA0E	JNZ	READ1	:EOT
0ECD C07A0A	CALL	STOP	:STOP TAPE
0EE0 C3860F	JMP	MAIN	:RETURN MAIN
0ED3 21CE40	LOAD:	H,MSGG6	:DISPLAY L> PROMPT
0EE6 C07F08	LXI	MSG	:
	CALL	MSG	:
0EE9 CDA50A	CALL	CLROX	:PLACE READ HEAD ON OXIDE
0EEC 210044	LXI	H, BUFR1	:PCINT TO RANDOM LOAD BUFFER
			:BEGINING ADDR 4400H
0EDF C36105	JMP	RCLCAD	:READ LOAD FRM CASSETTE TAPE
			:INTO ADDR 4400-7F00H
			:RLOAD WILL RETURN USER TO MAIN
0EE2 21C740	MASTER:	H,MSGG5	:DISPLAY E> PROMPT
0EE5 C07F08	LXI	MSG	:
	CALL	MSG	:
0EE8 CC9608	CALL	HEADNG	:
0EEB 2A9A42	LDA	TYPCTA	:
0EEE FE41	CPI	'A'	:

0EF0 C2030F	JNZ MSTR1	:	
0EF3 C0A605	CALL OXIDE	:	
0EF6 21803C	LXI H,MSG1	:	
0EF8 1601	MVI D,01H	:	
0EFE CE55	MVI C,95H	:	
0EFC CCE509	CALL WRREC	:	
0F00 C3100F	JMP MSTR2	:	
0F03 CCA609	MSTR1: CALL	:	
0F06 21153D	LXI F,MSG2	:	
0F08 1602	MVI D,02H	:	
0F0B 0E9A	MVI C,9AH	:	
0F0C CCE509	CALL WRREC	:	
0F10 CC7A0A	MSTR2: STOP	:	:DISPLAY NUMBER OF RUNS
0F13 216A42	CALL H,MSG1	:	
0F16 CC7F08	CALL MSG	:	
0F19 21463C	LXI H,REPEAT	:	:PCINT TO REPEAT ACDR
0F1C CC6608	CALL CONIN	:	:INPUT CHAR FRM CONSOLE
0F1F CC7208	CALL CCNCUT	:	:OUTFLT CHAR TO CONSOLE
0F22 4F	MOV C,A	:	
0F23 CDDF01	CALL CNVBN	:	:CONVERT ASCII TC BINARY
0F26 32463C	STA REPEAT	:	
0F29 217E40	LXI F,MSGF	:	
0F2C CC7F08	CALL MSG	:	
0F2F 21483C	LXI H,FSTCHN	:	
0F32 CC6608	CALL CONIN	:	
0F35 CC7208	CALL CONOUT	:	
0F38 4F	MOV C,A	:	
0F39 CDDF01	CALL CNVBN	:	
0F3C 32483C	STA FSTCHN	:	
0F3F 3E00	MVI A,X1	:	
0F41 32473C	STA GAIN	:	
0F44 3E00	MVI A,OH	:	
0F46 324A3C	STA KOUNTR	:	
0F49 21C043	LXI F,BUFR	:	
0F4C 224B3C	SHLD DATPTR	:	
0F4F 214F3C	LXI H,X	:	

OF52 36C0	MVI M,00H ;	
OF53 2C	L M,0H ;	
OF54 3600	MVI M,0H ;	
OF55 2C	L M,0H ;	
OF56 36C0	MVI M,0H ;	
OF57 2C	L M,80H ;	
OF58 3680	MVI M,00H ;	
OF59 2C	L M,00H ;	
OF5A 3600	MVI M,00H ;	
OF5B 2C		
OF5C 3600		
OF5D 2C		
OF5E 3600		
OF5F 2C		
OF60 3600		
OF61 3600		
OF62 3600		
OF63 CCE00C	MSTR3: RAMP ;	
OF64 CD7A0A	CALL STOP ;	
OF65 21463C	LXI H,REPEAT ;PCINT TO REPEAT STORAGE ADCR	
OF66 7E	MOV A,M ;PUT	
OF67 3C	DCR A REPEAT ;	
OF68 32463C	STA CH ;	
OF69 FE00	CPI CH ;	
OF70 FE00	JNZ MSTR3 ;	
OF71 C2E30F		
OF72 C2E30F		
OF73 C2E30F		
OF74 21B03D	LXI H,MSG3 ;	
OF75 1604	MVI D,04H ;	
OF76 0E0D	MVI C,00H ;	
OF77 0E0D		
OF78 0E0D		
OF79 0E0D		
OF7A 0E0D		
OF7B 0E0D		
OF7C 0E0D		
OF7D CDE509	CALL WRREC ;	
OF7E CD7A0A	CALL STOP ;	
OF7F CD7A0A		
OF80 CD7A0A		
OF81 CD7A0A		
OF82 CD7A0A		
OF83 C3860F	JMP MAIN ;	
OF84 C3860F		
OF85 C3860F		
OF86 21A440	MAIN: LXI H,MSGG ;	
OF87 CD7F08	CALL MSG ;	
OF88 21D540	LXI P,MSGH ;DISPLAY CONTROL G FOR INSTRUCTIONS	
OF89 CD7F08	CALL MSG ;	
OF8A CD7F08		
OF8B CD7F08		
OF8C CD7F08		
OF8D CD7F08		
OF8E CD7F08		
OF8F CD7F08		
OF90 CD7F08		
OF91 CD7F08		
OF92 31FF7F	MAIN1: LXI SP,7FFFF ;INITIALIZE STACK PCINTER	
OF93 31FF7F		
OF94 31FF7F		
OF95 C0B008	CALL DACZRO ;SET DAC'S TO ZERO	

OF98 DBED	IN	CRTS ;CHECK FOR DEPRESSED KEY
OF9A E602	ANI	RBR ;02H
OF9C CA920F	JZ	MAIN1 ;LOCP UNTIL KEY DEPRESSED
OF9F CD6608	CALL	CONIN ;READ DEPRESSED KEY
0FA2 CL7208	CALL	CONOUT ;ECHO KEY
0FA5 FE07	CPI	CNTRLG ;CHECK FOR CCNTFOL G
0FA7 CAA70B	JZ	GOUGE ;
0FAA FEC3	CPI	CNTRLC ;ENTER MONITOR
0FAC CA0800	JZ	MONITOR ;
0FAF FE13	CPI	CNTRLS ;CHECK FOR CCNTFCL S
0FB1 CA690D	JZ	SORCLD ;
0FB4 FE01	CPI	CNTRLA ;CHECK FOR CONTROL A
0FE6 CAB30D	JZ	ADCCHN ;
0FE9 FE02	CPI	CNTRLB ;CHECK FOR CCNTFOL B
0FBB CA320E	JZ	ADCMXM ;
0FBE FE12	CPI	CNTRLR ;CHECK FOR CONTROL R
0FC0 CAB10E	JZ	READ ;
0FC3 FE0C	CPI	CNTRL ;CHECK FOR CCNTFOL L
0FC5 CAD30E	JZ	LOAD ;
0FC8 FEC5	CPI	CNTRLE ;CHECK FOR CONTROL E
0FCA CAE20E	JZ	MASTER ;
0FCD C3920F	JMP	MAIN1 ;
CFDC	END	;

```

:"HEXLINK" IS A MODIFICATION OF "LINK" PROGRAM DEVELOPED BY
: LT. MACK T. ELLIOTT, USN. "LINK" IS DESIGNED TO INTERFACE THE
:MCS 80C (AND MODEL 40 PRINTER) WITH CP/CMS THROUGH A 1200
: BALC TELEPHONE LINE. MODIFICATIONS TO "LINK" ARE OUTLINE
: WITH ASTERISKS (****).

```

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```

" AND CNTLU
CPI XCF A
MOV C, A
CPI XON
JZ CTX
LDA PPREG
CPI 0
JZ CTX
MOV A, C
CALL DRIVER
CTX:
MOV A, C
CALL SEND
JMP TX
CHNG1: MVI A, ' '
RET
CHNG2: MVI A, 08H
CALL CONOUT
MVI A, ' '
RET
CHNG3:
CALL SEND
JMP RCV
CHNG4: MVI A, ' '
CALL CONOUT
RET
; RECEIVE MODE
RCV:
LCA PPREG
CPI 0
JZ CR
MVI A, CR
CALL DRIVER
MVI A, LF
CALL DRIVER
CRCV:
MVI A, XOFF
END OF LINE CHAR
CALL SEND
CRCV1:
;HL REGISTER POINTS TO ADDR FOR NEXT WORD RECEIVED
;DE REGISTER POINTS TO ADDR OF NEXT WORD TO BE PRINTED
LXI H, BUFF
;FIFO BUFFER ADDR FOR RECEIVED DATA

```

;CHECK IF PRINTER ON

;SENDS CHAR TO VIRTUAL MACHINE
;LOOPS FOREVER

;CHECK IF PRINTER ON

;START NEW LINE ON PRINTER

;END OF LINE CHAR

;HL REGISTER POINTS TO ADDR FOR NEXT WORD RECEIVED
;DE REGISTER POINTS TO ADDR OF NEXT WORD TO BE PRINTED
;FIFO BUFFER ADDR FOR RECEIVED DATA

```

RX1: LXI D, BUFF
      CALL BREAK
      INI 61H
      ANI 02H
      JZ CKPRT
      ;CHECK LINE FOR CHAR
      ;IF LINE NOT READY, CHECK IF BUFFER CAUGHT UP

RX:   IN 60H
      ANI 7FH
      CPI XON
      JZ CATCH
      CPI XOFF
      JZ RX1
      MOV M, A
      INX H
      JMP RX1
      ;INPUT WORD FROM LINE
      ;IF END OF LINE, LET BUFFER CATCH UP
      ;FILTER OUT XOFF CHAR
      ;STORE CHAR
      ;LOOP UNTIL END OF LINE
      ;STORE LAST WORD
      ;NEXT WORD TO BE PRINTED
      ;IF CAUGHT UP, GO BACK TO TRANSMIT MODE
      ;PRINT CN CONSOLE
      ;CHECK IF PRINTER ON

      BACK: INX D
      JMP LOOCP
      ;LOOP UNTIL CAUGHT UP

      GOUGE: LXI D, MSG2
      GLOCF: LCAX D
      CPI 1
      JZ TX
      CALL CONOUT
      MOV B, A
      LCA PPREG
      CPI 0
      JZ GLP
      MOV A, B
      CALL DRIVER
      GLP: INX C
      JMP GLOOP

```

```

PRTCCNT:  PPREG      ;CHECK IF PRINTER ON OR OFF
          LDA        0
          CPI        PRTOFF
          JNZ        USART2
          CALL       USART2
          MVI        A, 1
          STA        PPREG
          MVI        A, CR
          CALL       CR
          MVI        A, LF
          CALL       LF
          CALL       DRIVER
          JMP        TX
          ;SUBSEQUENT ROUTINES CHECK THIS ADDR
          ;START PRINTER ON NEW LINE

PRTOFF:   MVI        30H
          CUT        63H
          MVI        A, 0
          STA        PPREG
          JMP        TX
          ;SUBSEQUENT ROUTINES CHECK THIS ADDR

CRIVER:   PSW        ;DRIVES PRINTER USART
          PUSH       IN
          SLO:       63H
          RRC
          JNC
          POP        PSW
          CUT        62H
          RET
          ;CRIVES CCNSOLE USART

CONCUT:   PSW
          PUSH
          SLO2:      0F7H
          IN
          RRC
          JNC
          FCP
          CUT
          RET
          ;KEEPS TRACK OF WHICH RECEIVED DATA HAS BEEN PRINTED

CKPRT:   MOV        A,
          CMP        RX1
          JZ         0F7H
          IN
          RRC
          JNC
          LDA        PPREG
          CPI        CKP2
          JZ         63H
          IN
          RRC
          JNC
          RX1
          ;IF CAUGHT UP, NO NEED TO PROCEED

          ;IF CONSOLE NOT READY, NO NEED TO PROCEED
          ;CHECK IF PRINTER ON
          ;IF PRINTER NOT ON, NO NEED TO PROCEED
          ;IF PRINTER NOT READY, NO NEED TO PROCEED

```

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```

JNZ DLA2
RESET: MVI A, 37H
        CUI 61H
        RET

```

```

BOARC:

```

```

THIS ROUTINE INITIALIZES THE 534 BOARD, THE TIMERS, AND THE TWO USARTS
NEEDED TO DRIVE THE IBM HIGH SPEED LINE AND THE MODEL 40 PRINTER

```

```

BASE ADDR OF 534 BOARD      60H
CMC ADDR CF LINE USART      61H
DATA ADDR CF LINE USART     60H
CMC ADDR CF PTR USART       63H
DATA ADDR CF PTR USART      62H

```

```

TWO MORE USARTS AND ONE 8255 PARALLEL INTERFACE AND THEIR TIMERS ARE
AVAILABLE ON THE 534 BOARD. NEW INTERFACES MUST BE PROGRAMMED BEFORE USE

```

```

DI      6FH
CUI     6CH
CALL    TIMER
CALL    USART
EI
RET

```

```

;DISABLES 8080 INTERRUPTS
;RESETS BOARD
;SELECTS BOARD CONTROL BLOCK
;INITIALIZE PIT CHIPS
;INITIALIZE USARTS FOR IBM LINE AND PTR
;REENABLES INTERRUPTS

```

```

MUST SET UP TIMER CHIPS ACCORDING TO PAGE 3-12 OF 534 MANUAL
CHIP 0 HAS THREE TIMERS ON IT
TIMERS 0 AND 1 OF CHIP 0 ARE CONNECTED TO USARTS 1 AND 2
RESPECTIVELY, DRIVING THE IBM LINE AND THE PRINTER

```

```

TIMER: OUT 6CH
        MVI A, 36H
        CUI 63H
        MVI A, 40H
        CUI 60H
        MVI A, 0H
        CUI 60H
        MVI A, 76H
        CUI 63H
        MVI A, 8H

```

```

;PUTS BOARD INTO CONTROL ELCK
;SELECT TIMER 0 FOR LINE USART
;ADDR CF COUNTER 0 MCCE CONTROL
;SET A-40H IN TIMER C
;CLK/N=19.2KHZ FOR 1200 BAUC,
;BRF=16X
;SELECT TIMER 1 FOR PTR USART

```



```

CALL ANS
CALL FILE
CALL ANS
CALL TALLY
CALL TX
JMP FILERX:

; "FILES" FILE IN CMS
; PRINTS OUT RECCRD COUNT
; RETURNS TO TRANSMIT MODE
; SUBR PROMPTS CONSOLE FOR FILE TO BE RECEIVED, SETS UP FILE
; CONTROL BLOCK AND CREATES FILE ON FLOPPY DISK, RECEIVES FILE
; FROM CMS AND ECHOES ON CONSOLE, CLOSSES FILE AND RESTORES
; USER TO DIRECT CMS LINKUP

MVI A, 0
STA COUNT
CALL CPNAME+1
CALL CRLF
CALL RESTRT
CALL CKLF
CALL MAKE
CALL BETA
CALL CRLF
CALL HAUL
CALL FILEWR
CALL CLOSE
CALL TALLY
JMP RESTRT:

; SETS UP FILE CONTROL BLOCK
; DELETES AND CREATES DISK FILE
; PREPARES CMS TO TRANSMIT FILE
; RECEIVES FILE FROM CMS
; WRITES FILE ON DISK
; CLOSSES DISK FILE
; PRINTS RECCRD COUNT;
; RETURNS TO TRANSMIT MODE
; CLEAR OUT OLD FILE CONTROL BLOCK AND SETS UP NEW ONE
; MSG3
; PROMPTS "FILENAME.FILETYPE"
; PADS NEW FCB WITH "C(11 BLANKS)C000"
; BLANK CHAR

RESTRT: LXI D, MSG3
CALL MESSAGE
MVI A, 0
STA FCB2
LXI F, FCB2+1
MVI A, 20H
MVI B, 11
PADI: MOV M, A
INX H
DCR B
JNZ PAD1
MVI A, 0
MVI B, 4
LXI H, FCB2+12
PAD2: MOV M, A
INX H
DCR B
JNZ PAD2
MVI C, 1
CALL BDOS

```

ASKS FOR DESIRED DISK AND NOTIFIES DISK DRIVE

```

CPI 'A'
JZ AONE
CPI 'B'
JZ BONE
JMP REPEAT
AONE: MVI E, 0
      JMP DSK
BONE: MVI E, 1
      JMP DSK
DSK: MVI C, 14 ;CHANGES DISK DRIVE SELECTION
     CALL BDOS
     MVI C, 1
     CALL BDOS
     CPI ' '
     JNZ REPEAT
     MVI B, 9
     LXI H, FCB2+1
FNAME: PUSH B
       PUSH H
       MVI C, 1
       CALL BDOS
       POP H
       POP B
       CPI CNTLC
       JZ 00
       CPI CNTLD
       JZ DIRECT
       CPI CNTLU
       JZ DUMMY
       CPI ' '
       JZ FTYPE
       MOV M, A
       INX H
       LCR
       JZ REPEAT
       JMP FNAME
FTYPE: MVI B, 4
       LXI H, FCB2+9
FTYPE1: PUSH B
        PUSH F
        MVI C, 1

```

IF FILENAME EXCEEDS 8 CHAR, START OVER


```

CALL BDOS
POP H
CPI B
JZ CNTLC
CPI 00
JZ CNTLD
CPI DIRECT
JZ CNTLU
CPI DUMMY
JZ CR
RZ
MOV M, A
INX F
DCR B
JZ REPEAT
JZ FTYPE1
JMP
DUMMY: CALL CRLF
JMP RESTRT
REPEAT:
LXI MSG4 ; PROMPTS "REPEAT"
CALL MESSAGE
JMP RESTRT ; START OVER
CPNAME:
LXI MSG15
CALL MESSAGE
LXI D, BUFF40
NAME2:
PUSH D
MVI C, 1
CALL BDOS
POP D
CPI D
JZ CNTLC
CPI 00
JZ CNTLD
CPI DIRECT
JZ CNTLU
CPI DUMMY2
JZ CR
CPI NAME3
JZ NAME2
STAX D
INX D
JMP NAME2
NAME3:
MVI A, '$'
STA D
RET
DUMMY2:

```

; IF FILETYPE EXCEEDS 3 CHAR, START OVER

```

CALL CRLF
JMP CPNAME
DIRECT: LXI SP, STKBTM
        MVI A, XOFF
        CALL SEND
        JMP CRCV1
MAKE:   MVI C, 19 ;DELETES ANY OLD DISK FILE HAVING
        LXI D, FC82 ;FILENAME, FILETYPE LISTED IN NEW FCB
        CALL BDOS
        MVI C, 22 ;CREATES NEW DISK FILE NAMED ABOVE
        LXI D, FC82
        CALL BDOS
        CPI 255
        JZ NOROCM ;ZERO INDICATES FULL DISK
        XRA A ;ZEROES FILE RECORD COUNTER
        STA FC82+32
        RET
NOROCM: LXI D, MSG11 ;PRCMPTS "DISK FULL"
        CALL MESSAGE
        JMP TX
CRLF:   MVI C, 2 ;STARTS NEW LINE ON CCNSOLE
        MVI E, CR
        CALL BDOS
        MVI C, 2
        MVI E, LF
        CALL BDOS
        RET
CPEN:   LXI D, FC82 ;OPENS DISK FILE FCR READING
        LXI C, 15
        MVI C, BDOS
        CPI 255
        JZ BADF ;ZERO INDICATES NO SUCH FILE CN DISK
        XRA A ;ZEROES FILE RECCRD COUNTER
        STA FC82+32
        CALL CRLF
        RET
BADF:   LXI D, MSG5A ;PROMPTS "FILE ACT FOUND"
        CALL MESSAGE
        INX SP ;ADJLSTS STACK POINTER
        INX SP ;RETURNS TO TRANSMIT MODE
        JMP TX

```

```

MESSAGE:          ;PRINTS MESSAGE AT ADDR IN DE ON CONSOLE
MVI C, 9
CALL 8005
RET

FILERD:          ;READS ENTIRE DISK FILE INTO RAM STARTING AT
;BUFF (LIMITED TO 52K BYTES)
FILERD0: LXI H, FLIMIT
SHLC FCOUNT
LXI D, BUFF
FILERD1: PUSH D
MVI C, 26
CALL 8005
LXI D, FC82
MVI C, 20
CALL 8005
POP D
PUSH PSW
CALL CCOUNTER
LXI H, 80H
CAD D
XCHG
POP P
RNZ
LHLC FCGUNT
LCX H
SHLC FCGUNT
MOV A, H
CPI 0
JNZ FILERD1
INX
MVI A, XOFF
STA X
RET
FILEWR: LXI D, 0
CONT: MVI B, 80H
CALL CCOUNTER
INLC CP
PUSH D
INLOOP2: LDAX D
JZ LAST
INX
;IF ECF, THIS WILL EE LAST RECCRD WRITTEN
;IF NOT ZERC, EOF CCNTAINED IN LAST RECCRD
;ZERO IF BUFFER HAS BEEN EXCEEDED
;TEMPORARY EOF -- PROGRAM WILL TRANSMIT
;FIRST 52K BYTES OF FILE, THEN
;COME BACK TO READ MORE
;WRITES DISK FILE BY SAME ALGORITHM AS ABOVE
;MUST CHECK EACH RECCRD FOR EOF CHAR

```

```

CCR B INLOGP2
JNZ D
POP D
PUSH D
MVI C, 26 ;CHANGE CMA BUFFER ADDR
CALL B00S
LXI D, FCB2
MVI C, 21 ;WRITE ONE DISK RECCRD
CALL B00S
POP D
PUSH PSW
LXI H, 80H ;INCREMENT BUFF BY ECH
CALL B00S
XCHG
POP PSW
CPI 1
JZ ERR1 ;1 INDICATES DISK FULL
JMP CONT ;WRITE LAST DISK RECORD

LAST: POP D
MVI C, 26
CALL B00S
LXI D, FCB2
MVI C, 21
CALL B00S
CPI 1
JZ ERR1
RET

ERR1: LXI D, MSG13 ;PRCMPTS "DISK FULL"
CALL MESSAGE
RET

CLOSE: LXI D, ;CLOSES DISK FILE
MVI C, FCB2
CALL B00S
LXI D, MSG7 ;PROMPTS "TRANSMISSION COMPLETE"
CALL MESSAGE
RET

TALLY: LDA COUNT ;PRINTS CUM RECORD COUNT
RAR
RAR
RAR
ANI 0FH
ADI 30H
CALL CONOUT

```



```

RAL      B,A
MOV      B,A
FRX2:    CALL GETWC
         CALL OFEC7H
         JC   FRX2
         CRA B
         POP B
         *****
         STAX D
         INX D
         DCX B
         PCV A, B
         CPI 0
         JZ   EXCEED
         CALL BREAK2
         JMP  FRX1
GETb0:   IN  61H
         ANI 2
         JZ   GETWC
         IN  60H
         CPI XON
         JZ   MARK
         RET
OMIT:    CALL GETWC
         CPI LF
         RZ
         JMP OMIT
         *****
         BREAK2: *****
                     :CHECK KEYBOARD FOR INTERRUPT--
                     :IF INTERRUPT EXISTS, RESET STACK POINTER
                     :AND JUMP TO DIRECT LINKUP MODE
                     :WHERE INTERRUPT CONDITION WILL BE ACTED
                     :AND A SIGNAL SENT TO CMS
                     *****
IN  0F7H
ANI 2
RZ
LXI JMP SP, STKETM
MARK:    CRCV1
         *****
         :MARK END OF FILE WITH "EOF"
         :LAST CHARS RECEIVED ARE CR,LF,NULL,R:>
         :WANT TO BACK UP TO LAST VALID WORD
         B
         POP PSW
         MVI A, EOF
         STAX C

```

```

EXCEED: RET
        LXI D, MSG17 ; PROMPTS "BUFFER LIMIT EXCEEDED"
        CALL MESSAGE
        MVI A, EOF ; MARKS END OF FILE-REMAINDER OF FILE IS LOST
        STAX D
        RET

BETA:   LXI D, ; SENDS "PRINT " TO CMS
        MSG10

GAMMA:  LCAX D
        CPI ;$
        JZ DELTA
        CONOUT
        CALL SEND
        INX D
        JMP GAMMA

DELTA:  LXI D, ; SENDS "FILENAME FILETYPE" TO CMS
        BUFF40

EPSILON: LXI D,
        LDAX D
        CPI ;$
        RZ
        CALL CONOUT
        CALL SEND
        INX D
        JMP EPSILON

CMS:    ;SETS UP CMS TO RECEIVE FILE BY COMMANDING
        ;"EDIT FILENAME FILETYPE"
        LXI D, MSG5

CMS2:   LDAX D
        CPI ;$
        JZ CMS3
        CONOUT
        CALL SEND
        INX D
        JMP CMS2

CMS3:   LXI D, BUFF40

CMS4:   LDAX D
        CPI ;$
        JZ CMS5
        CONOUT
        CALL SEND
        INX D
        JMP CMS4

```

```

CMS5: MVI A, XOFF
      CALL SEND
      RET
      :ECHOES CMS ANSWER TO CONSOLE
ANS: IN 61H
      ANI 2
      JZ ANS
      IN 60H
      CPI XON
      RZ
      CPI XOFF
      JZ ANS
      CALL CONOUT
      JMP ANS
      :FILTERS OUT XOFF
ANS2: IN 61H
      ANI 2
      JZ ANS2
      IN 60H
      CPI XON
      RZ
      CPI XOFF
      JZ ANS2
      CPI CR
      JZ ANS2
      CPI LF
      JZ ANS2
      CPI '>'
      JZ ANS2
      CALL CONOUT
      JMP ANS2
      :TRANSMITS FILE TO CMS
XMIT: LXI D, MSG6 ;PROMPTS "TRANSMITTING"
      CALL MESSAGE
      CALL PAUSE ; DELAY 100 MICROSECS AT BEGINNING OF EACH LINE
      LXI D, BUFF
      MVI C, 83H ;132 BYTES
      :IF EOF, TRANSMISSION IS FINISHED
      LDA D
      CPI EOF
      JZ XMIT3
      CPI XOFF
      JZ XMIT4
      CPI CR
      JZ ENDLN
      :CLOSE OUT LINE AT CARRIAGE RETURN

```



```

CPI LF
JZ SKIP
CPI 05H
CZ CHNG1
PCV B, A
CALL SEND
DCR C
JZ ENDLN2
SKIP: INX D
CALL BREAK3
JMP XMIT2
XMIT3: LXI D, MSG7 ; PROMPTS "TRANSMISSION COMPLETE"
CALL MESSAGE
XMIT35: CALL PAUSE
MVI A, XOFF ; SENDS DOUBLE XOFF TO SHIFT
CALL SEND ; CMS FROM INPUT TO EDIT MODE
CALL ANS2 ; WAIT FOR ANSWER AND DELAY IN BETWEEN
CALL PAUSE
MVI A, XOFF
CALL SEND
XMIT4: CALL RET
; FOR FILES EXCEEDING 52K, PROGRAM SHIFTS
; CMS TO EDIT MODE AND ISSUES "SAVE" COMMAND
; AT THIS POINT - CMS SAVES TRANSMITTED DATA
; AND RETURNS TO INPUT MODE, AT WHICH TIME
; PROGRAM READS NEXT SECTION OF FILE AND TRANSMITS
XMIT35: CALL ANS
CALL PAUSE
LXI D, MSG19
XMIT5: LDAX D
CPI '$'
JZ XMIT6
CALL CONOUT
CALL SEND
INX D
JMP XMIT5
XMIT6: MVI A, XOFF
CALL SEND
CALL ANS
LXI D, MSG18
CALL MESSAGE
CALL FILERO
; PROMPTS "RELOADING"
; READ NEXT PART OF FILE FROM DISK

```

```

JMP XMIT
BREAK3: IN OF7H
        ANI 2
        RZ
        IN OF6H
        ANI 7FH
        CPI CNTLD
        RNZ
        JMP DIRECT
        ; SENDS XOFF AFTER EACH LINE
        ; IF LAST CHAR SENT WAS A CR, IGNORE-
        ; EFFECTIVELY CANCELS SKIPPED LINES
ENDLN:  B SKIP
        JZ
        ENDLN2: MOV A, XOFF
        MVI A, SEND
        CALL ANS2
        CALL PAUSE
        MVI C, 83H
        JMP SKIP
        ; 132 BYTES
        ; CONTINUE TRANSMITTING
        ; DELAY APPROX 100 MICROSECONDS
        PAUSE: LXI H, 200H
        MVI H, H
        MVI A, H
        CPI 0
        JNZ PAUSE2
        RET
        FILE: CALL PAUSE
        LXI D, MSG8
        FILE2: LOAX D
        CPI '$'
        JZ FILE3
        CALL CONOUT
        CALL SEND
        INX D
        JMP FILE2
        FILE3: MVI A, XOFF
        CALL SEND
        RET
        BUFF40: DS 20
        BUFF EQU 2
        END 100H
        ; BUFFER STARTS AT END OF PROGRAM

```

APPENDIX E: RANDOMIZED MIL SPEC 8866 SPECTRUM A LOADS

```

C
C
C
RANDOMIZED MIL SPEC 8866 SPECTRUM A LOADS
PO500010
PO50003C
PO500040
PO500050
PO500060
PO500070
PO500080
PO500090

INTEGER*2 KONST
INTEGER*2 KSTEP1
INTEGER*2 KSTEP2
INTEGER*2 KSTEP3
DIMENSION SMAX(4205),SMIN(4205),VOLTS1(4205),VOLTS2(4205),
1KSTEP1(4205),KSTEP2(4205),KSTEP3(8410)

C INPUT DATA
PO500100
PO500110
PO500120
PO500130
PO500140
PO500150
PO500160
PO500170

READ(5,32)NBLOCK,JLFVEL,SCALE,TLL,AREA
FORMAT(2I10,2F12.0,F12.3)
WRITE(5,34)NBLOCK,JLFVEL
FORMAT(//110,23H TIMES THROUGH BLOCK OF,110,6+ LOADS)
WRITE(5,33)SCALE,TLL,AREA
FORMAT(//10X,18H SCALE FACTOR =,F12.0,1X,6+ (FSI),//10X,18H LIMPOS00150
1IT LOAD =,F12.0,1X,6H (PSI),//10X,18H CROSS SECT AREA =,F12.3PO500160
1,1X,13H (SQ INCHES))
PO500170

C INITIALIZE PARAMETERS
PO500180
PO500190
PO500200
PO500210
PO500220
PO500230
PO500240
PO500250
PO500260
PO500270
PO500280
PO500290
PO500300
PO500310

IA=0
IE=0
IC=0
IK=0
IH=0
IJ=0
I=1
ITOTAL=0
KCNST=32768
PLL=TLL*AREA

C BEGIN RANDOMIZATION PROCESS
PO500320
PO500330
PO500340
PO500350
PO500360
PO500370
PO500380

IX=583
CALL RANDU(IX,IY,YFL)
IX=IY
IF(ITOTAL.EC.4201) GO TO 790
IF((YFL-GE.C.)-AND.(YFL-LT..1)) GC TO 750
IF((YFL-GE..1)-AND.(YFL-LT..2)) GC TO 751
IF((YFL-GE..2)-AND.(YFL-LT..3)) GC TO 752

```



```

750 IF((YFL.GE.:3).AND.(YFL.LT.:.4)) GC TO 753
IF((YFL.GE.:4).AND.(YFL.LT.:.5)) GO TO 754
IF((YFL.GE.:5).AND.(YFL.LT.:.6)) GC TO 755
IF((YFL.GE.:6).AND.(YFL.LT.:.7)) GO TO 756
IF((YFL.GE.:7).AND.(YFL.LT.:.8)) GC TO 757
IF((YFL.GE.:8).AND.(YFL.LT.:.9)) GC TO 758
IF((YFL.GE.:9).AND.(YFL.LT.:.1.)) GC TO 759
IF(IA.EQ.2) GO TO 700
ITCTAL=ITOTAL+1
IA=IA+1
SPAX(I)=1.25*PLL
SPIN(I)=.11*PLL
GC TO 785
751 IF(IE.EQ.4) GO TO 700
ITOTAL=ITCTAL+1
IB=IB+1
SPAX(I)=1.15*PLL
SPIN(I)=.11*PLL
GC TO 785
752 IF(IC.EQ.15) GO TO 700
ITOTAL=ITCTAL+1
IC=IC+1
SPAX(I)=1.05*PLL
SPIN(I)=.11*PLL
GC TO 785
753 IF(ID.EQ.44) GO TO 700
ITOTAL=ITCTAL+1
ID=ID+1
SPAX(I)=.95*PLL
SPIN(I)=.11*PLL
GC TO 785
754 IF(IE.EQ.136) GO TO 700
ITOTAL=ITOTAL+1
IE=IE+1
SPAX(I)=.85*PLL
SPIN(I)=.11*PLL
GC TO 785
755 IF(IK.EQ.250) GO TO 700
ITOTAL=ITCTAL+1
IK=IK+1
SPAX(I)=.75*PLL
SPIN(I)=.11*PLL
GC TO 785
756 IF(IG.EQ.45C) GO TO 700
ITOTAL=ITOTAL+1
IG=IG+1
SPAX(I)=.65*PLL
SPIN(I)=.11*PLL

```

```

POSCC39C
POSCC400
POSCC410
POSCC420
POSCC43C
POSCC440
POSCC450
POSCC460
POSCC470
POSCC48C
POSCC490
POSCC500
POSCC51C
POSCC520
POSCC530
POSCC540
POSCC550
POSCC560
POSCC57C
POSCC580
POSCC590
POSCC600
POSCC610
POSCC62C
POSCC63C
POSCC64C
POSCC650
POSCC660
POSCC670
POSCC680
POSCC690
POSCC70C
POSCC710
POSCC720
POSCC73C
POSCC740
POSCC75C
POSCC76C
POSCC770
POSCC780
POSCC790
POSCC800
POSCC810
POSCC820
POSCC830
POSCC84C
POSCC85C
POSCC860

```



```

757      GC TC 785
          IF(IH.EQ.650) GO TO 700
          ITCIAL=ITCTAL+1
          IH=IH+1
          SMAX(I)=.55*PLL
          SMIN(I)=.11*PLL
          GO TO 785
758      IF(II.EQ.950) GO TO 700
          IITOTAL=IITOTAL+1
          II=II+1
          SMAX(II)=.45*PLL
          SMIN(II)=.11*PLL
          GO TO 785
759      IF(IJ.EC.1700) GO TO 700
          IITOTAL=IITOTAL+1
          IJ=IJ+1
          SMAX(IJ)=.35*PLL
          SMIN(IJ)=.11*PLL
          GC TO 785
785      CONTINUE

C      FORM TWO VECTORS (PCISITIVE LOADS AND 11 PERCENT LCADS
          VCLTS1(I)=(SMAX(I)/SCALE)*10.
          KSTEP1(I)=(INT(VOLTS1(I))*(204.8)))*16+KONST
          VCLTS2(I)=(SMIN(I)/SCALE)*10
          KSTEP2(I)=(INT(VOLTS2(I))*(204.8)))*16+KONST
          I=I+1
          GC TO 700
          CONTINUE
790

C      ALTERNATE PCISITIVE LOADS WITH 11 PERCENT LOADS
          K=0
          J=-1
          DC 99 I=1,4205
          J=J+2
          K=K+2
          KSTEP3(J)=KSTEP1(I)
          KSTEP3(K)=KSTEP2(I)
          CGTINUE
99

WRITE RESULTANT VECTOR AS DEFINED BY DATA DEFINITION

          WRITE(9,120)(KSTEP3(I),I=1,8410)
          REWIND 9
          FORMAT(255A2,1A2)
120      $1CP

```

POS00870
 POS00880
 POS00890
 POS00900
 POS00910
 POS00920
 POS00930
 POS00940
 POS00950
 POS00960
 POS00970
 POS00980
 POS00990
 POS01000
 POS01010
 POS01020
 POS01030
 POS01040
 POS01050
 POS01060

POS01070
 POS01080
 POS01090
 POS01100
 POS01110
 POS01120
 POS01130

POS01140
 POS01150
 POS01160
 POS01170
 POS01180
 POS01190
 POS01200
 POS01210

POS01220
 POS01230
 POS01240
 POS01250

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